Mitigating tap-changer limit cycles in modern electricity networks embedded with local generation units

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Mitigating Tap-changer Limit Cycles in Modern Electricity Networks Embedded with Local Generation Units

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Abstract—Cascaded on-load tap changers (OLTC) are widely used for coarse voltage control in electric power systems. There could be an interaction between load dynamics and OLTC control under certain system operating conditions which may lead to OLTC limit cycle phenomena thereby resulting into long term voltage oscillations in the system. In the recent years, renewable and non-renewable local generation units have been getting interconnected in modern power systems. The existence of OLTC limit cycles in the presence of local generation units has not been addressed in the literature. In this paper, the OLTC limit cycle phenomena, which can occur due to interactions among load-voltage dynamics, OLTC controls and the local generation, has been investigated. Also, a strategy is proposed for mitigating the power system oscillations which can be induced by OLTC limit cycles, especially with local generation units in the network. The proposed mitigation strategy including detailed investigations and analyses have been verified for a two-bus system, and successfully tested on a multi-bus system with local generation units using MATLAB.

Index Terms--Limit-cycle phenomena; load dynamics; local generation; on-load tap changer; power system oscillations.

I. INTRODUCTION

Limit cycles attributed to the operation of on-load tap changers (OLTCs) can occur in electricity networks under certain operating conditions mainly due to the interaction between load dynamics and OLTC controls. Eventually, these limit cycles in case of cascaded tap changers may result into sustained long term oscillations in the power systems. It may also affect the long term voltage stability of the network.

In [1], the existence of OLTC limit cycles is investigated and analyzed. The system load level, degree of reactive power compensation and the load voltage dependency are identified as the key parameters for initiation and avoidance of the OLTC limit cycles. The nature of limit cycles caused by the interaction between transformer tap changer and load dynamics is analyzed in [2]. A linearization of Poincaré map is used to analyze the local stability in the system under OLTC limit cycles. In [3], voltage oscillations in power systems with cascaded multiple OLTC units have been studied, where the focus has been on the limit cycles due to interactions between the tap changer and the load. Also, a control strategy is proposed in [3] to mitigate the OLTC limit cycles. It is based on adjusting the dead-band (DB) of the tap changer, which typically depends on the load characteristics. It has been found that the existing limit cycles will be avoided and a steady state condition is reached given a sufficiently large DB in case of stable load dynamics when tap ratios are fixed. On the other hand, the existing limit cycles will not be removed by increasing DB in case of unstable load dynamics wherein tap ratios are fixed. Moreover, it has been found that adjusting OLTC control parameters such as time delay and/or DB size may not have any effect on the existence of limit cycles under certain system conditions [1]. It may not be possible to avoid limit cycle behavior simply by retuning the OLTC dead-band limit and/or time delay. However, none of the studies in the literature have investigated and analyzed the OLTC limit cycle phenomena in medium voltage (MV) networks with higher penetration of renewable and non-renewable local generation (LG) units. For such networks, OLTC limit cycles can occur frequently due to interactions among load and voltage dynamics, OLTC control and the local generation.

In this paper, OLTC limit cycle phenomena in case of MV networks with higher penetration of LG units is investigated and analyzed thoroughly. The small signal model and describing function method used in [1] for OLTC limit cycle analysis in a two bus system have been extended for analyzing and predicting OLTC limit cycles in different multi bus system topologies with LG units. Also, a strategy based on coordinated VAr support from LG units and capacitor banks (CBs) is proposed to mitigate the OLTC limit cycles in the presence of LG units. It is easily implementable with a typical voltage control scheme. In this paper, MATLAB is used for modeling the sample system and conducting associated simulation studies.

This paper is organized as below. Section II outlines the theoretical background of investigating and analyzing the OLTC limit cycles and Section III elaborates the algorithm of the proposed strategy for mitigating OLTC limit cycles including realistic case study, while the concluding remarks have been made in Section IV of the paper.
II. BACKGROUND THEORY

A. Predicting Existence of OLTC Limit Cycles

Eigen value analysis is used to predict the existence of OLTC limit cycles, and the results are compared with describing function analysis. For large MV power systems, network reduction methods can be applied to minimize the computational burden [4].

1) Modeling Aspects

The model described by the dead band-ordinary differential equation (DB-ODE) is used for modeling OLTC as given by (01) [5]. It is noted that discrete tap steps are not taken into account in this OLTC model.

\[
\begin{align*}
\frac{dn}{dt} &= \left\{ \begin{array}{ll}
(-1) \frac{1}{T} (V_{LC} - V_{set} - DB / 2) & \text{if } (V_{LC} - V_{set}) > DB / 2 \\
(-1) \frac{1}{T} (V_{LC} - V_{set} + DB / 2) & \text{if } (V_{LC} - V_{set}) < (-) DB / 2 \\
0 & \text{if } |V_{LC} - V_{set}| < DB / 2
\end{array} \right.
\end{align*}
\]

(01)

where \( V_{LC} \) denotes regulated voltage at the regulating point, \( V_{set} \) is the voltage set value, \( T \) is the controller time delay and \( n \) is the transformer tap-ratio.

Accurate modeling of different load characteristics is one of the key requirements of analyzing and predicting OLTC limit cycles. In this paper, the loads are modeled as exponential recovery loads as given by (02) and (03) [4].

\[
\begin{align*}
\dot{x}_p &= \frac{1}{T_p} \left( -x_p + P_s (V) - P_s (V) \right) \quad P_s (V) = k_L P_0 (V)^{a_L} \\
P_s (V) &= k_L P_0 (V)^{a_L} \\
x_q &= \frac{1}{T_q} \left( -x_q + Q_s (V) - Q_s (V) \right) \quad Q_s (V) = k_L Q_0 (V)^{b_L} \\
Q_s (V) &= k_L Q_0 (V)^{b_L}
\end{align*}
\]

(02) (03)

where, \( x \) is an internal state which models the load recovery dynamics. The recovery time constants are \( T_p \) and \( T_q \), and \( a_L, b_L, \alpha, \beta \) are the exponents of the voltage. The steady state nodal voltage dependency is denoted using \( P_s (V) \) and \( Q_s (V) \), where the transient (instantaneous) nodal voltage dependency is denoted using \( P_s (V) \) and \( Q_s (V) \) respectively. The \( P_d \) and \( Q_d \) denote actual loads where the rated load values are denoted using \( P_0 \) and \( Q_0 \). The load scale factor is \( k_L \).

It is assumed that the LG units respond instantaneously to the system changes. The respective power injections of LG units have been incorporated in the power balance equations. The active power response of LG unit is \( P_{LG} \) whereas the reactive power response is \( Q_{LG} \).

The active power function \( N(A) \) of the dead-band \( (DB) \) in the DB-ODE model of the OLTC can be derived as given by (04) [11, 6]. The amplitude of any sinusoidal input is \( A \), where periodic OLTC limit cycles are assumed to be approximately sinusoidal. The condition associated with the occurrence of OLTC limit cycle phenomenon is given by (05), where the small signal model of the equivalent system is given by (06).

\[
N(A) = \begin{cases} 
1 - \frac{2}{\pi} \sin^{-1} \left( \frac{DB}{2A} \right) + \frac{DB}{2A} \sqrt{1 - \left( \frac{DB}{2A} \right)^2} & \text{if } A > DB / 2 \\
0 & \text{if } A < DB / 2
\end{cases}
\]

(04)

Condition for limit cycle phenomenon:

\[
G(j\omega) = (-\frac{1}{N(A)}) \quad \forall \quad G(j\omega) = (-) G_n(j\omega) \times G_e(j\omega)
\]

(05)

where \( G_n (s) \) → transfer function of the model associated with OLTC, which is derived from the state space model given by (06) \( G_e (s) \) → transfer function of the ODE part of DBODE model

2) Case Study for a Two Bus System

The two bus system shown in Fig. 1 is used for investigating and analyzing OLTC limit cycle phenomenon under different system operational states.

![Fig. 1. Two bus system model.](image)

The proposed mathematical model of the power system used for analyzing OLTC limit cycles is given below.

Power system equations including dynamics:

\[
\dot{x} = f(x, v), \quad g(x, v, P_{LG}, Q_{LG}, u) = 0
\]

\( \delta \) = voltage phasor angle, \( V \) = voltage magnitude

State space model of the power system:

\[
A \dot{x} = \left( \frac{\partial f}{\partial x} \right) \dot{x} + \left( \frac{\partial g}{\partial v} \right) \dot{v}
\]

\[
\begin{align*}
\frac{\partial g}{\partial x} \dot{x} + \frac{\partial g}{\partial v} \dot{v} + \frac{\partial g}{\partial u} \dot{u} &= 0 \\
A \dot{x} &= (A)x + (B)u
\end{align*}
\]

(06)

State matrix : \( x = [x_p \ x_q]^T \), Input matrix : \( u = [u] \)

Output matrix : \( v = [V \ \delta]^T \)

\[
A = \begin{pmatrix} \frac{\partial f}{\partial x} & \frac{\partial f}{\partial v} \\ \frac{\partial g}{\partial v} & \frac{\partial g}{\partial u} \end{pmatrix}, \quad C = \begin{pmatrix} \frac{\partial g}{\partial x} & \frac{\partial g}{\partial u} \end{pmatrix}, \quad B = \begin{pmatrix} \frac{\partial f}{\partial v} \\ \frac{\partial g}{\partial v} \end{pmatrix}
\]

(07)

The existence of OLTC limit cycles in the presence of LG unit has been tested for different load demand levels, and the
key results of some example simulations are summarized below. The Nichols plots of both left and right hand side functions are used to solve the equation (05). The sample load and system data, used for simulation purposes, are $P_o = 106.8$ MW, $Q_o = 43.2$ MVAr, $X = 0.10641$ pu, $\alpha_l = 1$, $\beta_l = 0$, $\alpha_t = 1$, $\beta_t = 4$ and $T_p = T_q = 60$ s. The tap changer controller time delay ($T$) is 30 s. The simulated voltage change per tap operation is 0.0010 pu. The initial tap position of the OLTC is set at its nominal position for all simulations. The peak load demand is 96.005 MVA, where $k_L = 1, 0.9, 0.8, 0.7, 0.6, 0.5, 0.4$ and 0.3. The sending end bus voltage is 1.01 pu. Fig. 2 shows the respective Nichols plots for two bus system operation without local generation unit (case-01). According to the Nichols plots (Gki, where $i = 1, \ldots, 8$), it can be seen that the plots do not intersect the Nichols plot of $-1/N(A)$ function for different values of $k_L$, which demonstrates that OLTC limit cycles do not exist for the test system without local generation. The plot (GA), shown by the orange color vertical line, represents the Nichols plot of $-1/N(A)$ function.

According to the investigations, it can be seen that there can be OLTC limit cycles where active power generation level of the LG unit exceeds 26.5 MW and $k_L = 0.3$ as shown in Fig. 3 (case-02).

The Fig. 4 illustrates an example OLTC limit cycle phenomenon in real time for the predicted case-02 shown in Fig. 3. This is obtained by solving the first order differential equations of x-states which models the load recovery dynamics. In this case, the power output of the LG unit is assumed to be constant, where mechanical time delay of OLTC is assumed to be 6 s. The time domain simulation studies highlight the suitability of describing function method for predicting OLTC limit cycles in electric power systems.

The Fig. 5 shows that the OLTC limit cycles may recur frequently if active power generation level of the LG unit exceeds 87.5 MW (case-03).

Importantly, it is very clear that the recurrence of OLTC limit cycles in the presence of a LG unit under any practical system operation can be high, as predicted, compared to the system operation without a LG unit, due to variation and intermittency in the power output of LG unit and associated change in dynamics. In case of multiple OLTC units, hunting among OLTCs is another issue. In such a situation, predicted limit cycles for a particular OLTC may not be of sustained nature and it may be diminished after the next tap operation in the system. Also, the limit cycles may sustain for a longer time due to the variation in the power output by a LG unit and the associated change in system dynamics, especially when the load factor, $k_L$ do not vary (flat load profiles) or has slow ramp variation. Therefore, an implementation strategy for mitigating OLTC limit cycles in the presence of LG units may be essential for networks with high penetration of local generation.
The reactive power support of 13.5 MVAr by the LG unit in Fig. 1 can prevent the system from an oscillatory response, attributed to OLTC limit cycles, which can occur when the real power output of the LG unit is 26.5 MW and $k_L = 0.3$ as shown in Fig. 6 (case-04). It is an indicative of the fact that OLTC limit cycles may be mitigated by implementing a coordinated VAr management scheme in the system, comprising of Volt/VAr support by the LG unit.

When the LG unit absorbs reactive power (case-05), the intersection point of the associated Nichols plots i.e., $G_{k_i}$ (where $k_L = 0.3$) is shifted downward as shown in Fig. 7 along the GA curve to a lower open loop gain compared to the case-02. It means that the absorption of reactive power by the LG unit affects $G(j\omega)$ function ($G_{k_i}$) as a gain factor.

Consequently, the amplitude of limit cycles is changed, but not the frequency. Moreover, shifting the curve below the point (0 dB, -180°) can remove a limit cycle, but it may lead to instability of the closed loop system. In summary, it is clear that there would be a certain LG penetration level which can create OLTC limit cycles, and also which can mitigate OLTC limit cycles for each operational state of the system.

Finally, the eigen values of overall system (i.e. combined ODE part of the OLTC model and power system model) state matrix for above different operations (case-01 to case-05) are derived and shown in Table I. The unstable scenarios with OLTC limit cycles, where at least one of the eigen values has a positive real part are highlighted. The results of the eigen value analysis are very much in agreement with the results obtained using describing function method, which has been implemented for predicting the existence of OLTC limit cycles. Moreover, a modal analysis can be done using the proposed small signal model in order to identify the oscillatory modes referred to OLTC limit cycle instability; which is out of the scope of this paper.

**B. Case Study for a Multi Bus System**

In this case study, OLTC limit cycle phenomenon is investigated and analyzed for a multi-bus system, as shown in Fig. 8, for different system operational states, and one of the simulated cases is presented in this paper. The describing function analysis and eigen value analysis are carried out, and compared for multi bus systems in order to further test the applicability of the mathematical model derived under Section II-A for predicting the OLTC limit cycles. The multi-bus system of Fig. 8 is derived from [7] and modified by adding load dynamics, control data and the line data (i.e., $R_5 = 0.00192$ pu and $X_5 = 0.04256$ pu) related to connecting the LG unit.
TABLE I

<table>
<thead>
<tr>
<th>$k_1$</th>
<th>1.0</th>
<th>0.9</th>
<th>0.8</th>
<th>0.7</th>
</tr>
</thead>
<tbody>
<tr>
<td>Case-01</td>
<td>-0.0010</td>
<td>-0.0099</td>
<td>-0.0008</td>
<td>-0.0007</td>
</tr>
<tr>
<td>Case-02</td>
<td>-0.0007</td>
<td>-0.0006</td>
<td>-0.0005</td>
<td>-0.0004</td>
</tr>
<tr>
<td>Case-03</td>
<td>+0.0008</td>
<td>+0.0001</td>
<td>+0.0002</td>
<td>+0.0003</td>
</tr>
<tr>
<td>Case-04</td>
<td>-0.0007</td>
<td>-0.0006</td>
<td>-0.0005</td>
<td>-0.0004</td>
</tr>
<tr>
<td>Case-05</td>
<td>-0.0007</td>
<td>-0.0006</td>
<td>-0.0005</td>
<td>-0.0004</td>
</tr>
<tr>
<td>$k_2$</td>
<td>0.6</td>
<td>0.5</td>
<td>0.4</td>
<td>0.3</td>
</tr>
</tbody>
</table>

The small signal model of the multi bus power system is given by (07).

System equations including dynamics:

$x = f(x, v)$, $g(x, v, P_{LG}, Q_{LG}, u) = 0$

State space model:

$\dot{x} = (A_1)x + (B_1)u$

$A_1 = (A_1)\Delta x + (B_1)\Delta n$

$B_1 = (C_1)\Delta x + (D_1)\Delta n$

State matrix: $x = [x_1, x_2, x_3, x_4]^T$

Input matrix: $u = [1]$

Output matrix: $v = [V_1, V_2, V_3, V_4, \delta_2, \delta_3, \delta_4]^T$

$A_1 = \left( \frac{\partial f}{\partial x} \right) + \left( \frac{\partial g}{\partial x} \right) \Delta x$

$B_1 = \left( \frac{\partial f}{\partial u} \right) + \left( \frac{\partial g}{\partial u} \right) \Delta u$

For the tap changer model, input is the transformer secondary bus voltage magnitude, $V_2$ whereas output is the tap ratio, $n$. The OLTC limit cycles can be predicted as shown in Fig. 9, where the simulated load and system data are as below:

total $P_0 = 94.0$ MW, total $Q_0 = 21.0$ MVAr, $\alpha = \beta = 1$, $\alpha_2 = 2$, $\beta_2 = 4$, $T_p = 120$ s, $T = 60$ s and OLTC controller time delay, $T = 30$ s. The total active and reactive power outputs by the LG unit are 34.6 MW and 5.3 MVAr, respectively. Initial tap position of OLTC is '1' in the direction of increasing voltage, where taps are incorporated in the primary winding of the substation transformer. The total peak load demand is around 90.0 MVA, where $k_2 = 0.85$ and the grid voltage is 1.0 pu.

The eigen values derived using overall system state matrix with the VAr support by the LG unit are shown in Table II.

TABLE II

| $0.0010 + 0.0008i$ | $0.0010 - 0.0008i$ | $0.0186$ | $0.0174$ | $0.0078$ | $0.0081$ |

They are indicative of the fact that OLTC limit cycles can also exist with the LG unit operating in voltage control mode, especially when the control action of the LG unit has not been coordinated with the operation of other voltage regulating devices. Also, this simulation shows the applicability and suitability of proposed eigen value analysis for predicting OLTC limit cycles in multi bus systems. Therefore, the proposed strategy in section-III for mitigating OLTC limit cycles is mainly based on the proposed mathematical model given by (06) and the associated eigen value analysis which can be used for predicting OLTC limit cycles in electricity networks with multiple LG units.

Fig. 8. Multi bus system model.

Fig. 9. Nichols plots of $G(j\omega)$ for an existence of OLTC limit cycles in the multi bus system when $k_2 = 0.85$, $P_{LG} = 34.6$ MW and $Q_{LG} = 5.3$ MVAr.

III. PROPOSED CONTROL STRATEGY FOR MITIGATING OLTC LIMIT CYCLES IN PRESENCE OF LG UNITS

From power system control perspective, the system operating conditions are normally classified into five states such as normal, alert, emergency, extreme emergency and restorative [8]. In the proposed strategy for mitigating OLTC...
limit cycles, the control transition is between normal and the alert states. The system enters the alert-state form the normal-state, if existence of OLTC limit cycles is predicted. Then, the preventive control action based on the proposed control strategy is activated to mitigate the OLTC limit cycles while controlling the system voltage. Since the objectives of normal state voltage control can be different and incorporating those objectives with mitigating OLTC limit cycles may not always be effective, this transition based voltage control is used. Also, it is proposed to embed the proposed control module in a centralized voltage control scheme as additional hardware and software for updated operation, thereby ensuring effective voltage control.

A. Proposed Strategy

This is mainly based on coordinated VAr support from LG units and CBs for avoiding the conditions which have to be satisfied for the existence of OLTC limit cycles. From (06) and for particular operation of OLTCs and CBs, it can be seen that the matrix-A is a function of nodal voltage magnitudes and phase angles as given by (08).

\[
A = \left[ \frac{\partial f}{\partial x} \right] - \left[ \frac{\partial f}{\partial y} \right] \left[ \frac{\partial f}{\partial x} \right]^{-1} \left[ \frac{\partial f}{\partial x} \right] \left[ \frac{\partial f}{\partial y} \right] = f(V, \delta) \rightarrow A = f(V, \delta) \quad (08)
\]

Hence, by means of voltage control through coordinated VAr management in the system, a stable system operation without system oscillations, typically induced by OLTC limit cycles, can also be achieved. Accordingly, the proposed mitigating strategy is designed. The step-by-step algorithm of the proposed strategy is outlined below.

Step-01: From the on-line measurements and information sent by distribution management system (DMS), the control module is executed.

Step-02: For the current state of the system, the overall system state matrix is updated and the respective eigen values are derived.

Step-03: If all the eigen values have negative real part, the normal state voltage control module is enacted.

Step-04: If at least one eigen value has a positive real part, the alert state voltage control module is enacted.

Step-05: The sensitivity matrix, \( S_{M} \), given by (09) is derived. The sensitivity values of VAr supports by the LG unit and the CB are \( S_{MLG} \) and \( S_{MCB} \), respectively, where \( \Delta V \) is voltage deviation for small change of the LG unit’s reactive power, \( \Delta Q_{LG} \) and CB’s reactive power, \( \Delta Q_{CB} \).

\[
\Delta V = \left[ S_{MLG} \right] \left[ \frac{\Delta Q_{LG}}{\Delta Q_{CB}} \right] \quad S_{M} = \left[ S_{MLG} \right] \left[ S_{MCB} \right] \quad (09)
\]

Step-06: The order of VAr support devices (i.e. LG units and CBs) which are going to be utilized for coordinated VAr support is determined using the associated sensitivity values derived in Step-05, capability of devices for supporting the system voltage and the amount of VAr support. The generalized sequence in terms of time delays, \( T \) is given by (10).

\[
T_{LG-\text{larger}} < \ldots < T_{LG-\text{smaller}} < T_{CB-\text{larger}} < \ldots < T_{CB-\text{smaller}} < T_{OLTC_{\text{upstream}}} < \ldots < T_{OLTC_{\text{downstream}}} \quad (10)
\]

Step-07: The new VAr reference values for selected VAr support devices (i.e. LG units and CBs) are identified subject to system constraints and capability limits of the LG units and CBs, where objective is to ensure stable system operation without OLTC limit cycles and maintain the system voltage within stipulated limits.

Step-08: The updated VAr reference values are assigned for local controllers of LG units and CBs.

Step-09: The OLTC local controllers are enabled.

Step-10: For the subsequent instances of time (i.e., \( t = t + 1 \)), repeat the procedure from Step-01.

Flow chart of the proposed voltage control algorithm prior to enacting OLTC tap operations is shown in Fig. 10.

The voltage control strategy proposed in [9] can be used for normal state voltage control in conjunction with the proposed strategy of mitigating OLTC limit cycles. It is an online voltage control strategy which is designed and tested for correcting the system voltage with control-coordination ascertaining voltage support by LG units in the system. It also ensures prioritized operation of the voltage support operation of LG units and the voltage regulating devices, and aids in...
blocking simultaneous operations, thereby minimizing the total tap operations. However, even under this normal state voltage control only, there could be a possibility of recurrence of OLTC limit cycles.

Design of the proposed control module contains the embedded mathematical model of the power system, model of the proposed control logic, search engine and the decision making control layer for enacting the VAr controllers of LG units, CBs and the tap operations of OLTCs. The search engine based on the proposed control algorithm, as detailed in the flow chart in Fig. 8, is adopted in order to determine the control parameters of LG units and CBs.

The practical implementation strategy for proposed control is outlined in Fig. 11. The proposed control modules are embedded in a grid centered DMS for on-line voltage control in the given MV test system. The control panels (CPs) of LG units and voltage regulating devices are proposed to be equipped with supervisory control and data acquisition (SCADA) facilities.

![Fig. 11. Topology of the on-line implementation of proposed control strategy (with reference to the test system depicted in Fig. 12).](image)

**B. Test Case Study**

1) Control Model

In this case study, a MV test system with cascaded OLTCs is considered. This sample test system is operated with 3 OLTCs, LG units and a CB for Volt/VAr correction, and its topological model is given by Fig. 12. In this test system, OLTC limit cycles can be induced not only due to interaction among load dynamics, OLTC control and the power generated by LG units, but also due to interaction of CB. This CB is modeled using its susceptance value, $B$.

![Fig. 12. Model of the test power system used for control model.](image)

The small signal model of the test power system model incorporated in the control model is given by (11).

System equations including dynamics:

$$ x = f(x, v), \quad \dot{x} = f(x, v, P_{LG}, Q_{LG}, B, u) = 0, \quad Q_{CB} = B.V_i^2 $$

State space model:

$$ \dot{x} = \left( \frac{\partial f}{\partial x} \right) Ax + \left( \frac{\partial f}{\partial v} \right) Av + \left( \frac{\partial f}{\partial u} \right) Au = 0 $$

$$ \dot{x} = (A_2 \cdot \dot{x} + (B_2) Au $$

$$ \dot{v} = (C_2 \cdot \dot{x} + (D_2) Au $$

(11)

State matrix : $x = \begin{bmatrix} x_{p2} & x_{q2} & x_{p3} & x_{q3} & x_{p4} & x_{q4} \end{bmatrix}^T$

Input matrix : $u = \begin{bmatrix} n_1 & n_2 & n_3 \end{bmatrix}^T$

Output matrix : $v = \begin{bmatrix} V_2 & V_3 & V_4 \end{bmatrix}^T$

$$ A_2 = \left( \frac{\partial f}{\partial x} \right) - \left( \frac{\partial f}{\partial v} \right) \left( \frac{\partial g}{\partial v} \right)^{-1} \left( \frac{\partial f}{\partial u} \right), \quad C_2 = (-) \left( \frac{\partial g}{\partial v} \right)^{-1} \left( \frac{\partial g}{\partial u} \right) $$

$$ B_2 = (-) \left( \frac{\partial f}{\partial v} \right) \left( \frac{\partial g}{\partial v} \right)^{-1} \left( \frac{\partial g}{\partial u} \right), \quad D_2 = (-) \left( \frac{\partial g}{\partial v} \right)^{-1} \left( \frac{\partial g}{\partial u} \right) $$

2) Test Results

In the test case study, different system operational states are simulated and one of the cases is presented. In this state, a possibility of limit cycles in OLTC ($n_1$) and OLTC ($n_2$) is predicted according to Step-01 to Step-03 of the proposed control algorithm detailed earlier in Section III-A. The describing function analysis shown in Fig. 13 confirms the results given by proposed eigen value analysis which are shown in Table III. As discussed in Section II-A, the predicted OLTC limit cycles may or may not be sustained. However, enacting a mitigating strategy would be essential in a network, such as above test system, with a significant penetration of local generation, because there is a higher possibility for sustaining the predicted OLTC limit cycles due to intermittency in power outputs of the LG units and associated change in dynamics.

The simulated load scale factors for load 2, 3, and 4 are $k_{l2} = 0.8$, $k_{l3} = 0.9$, and $k_{l4} = 0.9$, where total peak load demands are 80 MVA, 12.800 MVA and 3.128 MVA, and total $P_{LG} = 86.4 MW$, $Q_{LG} = 41.8454 MVAR$, $P_{CB} = 13.824 MW$, $Q_{CB} = 6.6953 MVAr$, $P_{CB} = 3.456 MW$ and $Q_{CB} = 1.6738 MVAr$, respectively. The rating of the CB is 40 MVAR and simulated VAr support is 20 MVAR. The simulated initial tap positions of OLTC ($n_1$), ($n_2$), and ($n_3$) are 2, 4 and 4 respectively in the direction of increasing voltage, where the controller time delays are 30 s, 45 s and 60 s respectively. The simulated active and reactive power generations of LG1, LG2 and LG3 units are (33,000 MW, 9,300 MVAr), (6,500 MW, 0 MVAr) and (1,600 MW, 0 MVAr), respectively. The simulated load parameters of load 2, 3 and 4 are ($\alpha_{l2} = 1.5$, $\beta_{l2} = 4.5$, $\alpha_{l3} = 2$, $\beta_{l3} = 3$, $P_{l2} = 174 s$, $P_{l3} = 84 s$), ($\alpha_{l4} = 2.5$, $\beta_{l4} = 5.5$, $\alpha_{l4} = 4$, $\beta_{l4} = 1.5$, $P_{l4} = 201 s$, $P_{l4} = 48 s$) and ($\alpha_{l4} = 1$, $\beta_{l4} = 3.5$, $\alpha_{l6} = 6$, $\beta_{l6} = 2$, $P_{l6} = 121 s$, $P_{l6} = 64 s$), respectively. The line data as shown in Fig. 10 are $z_0 = (0.0119 + j0.0550)$ pu, $z_2 = (0.0011 + j0.0950)$ pu, $z_2 = (0.1510 + j0.6721)$ pu, and $z_2 = (0.1989 + j2.6565)$ pu, respectively. The simulated grid voltage is 1.01 pu.
The voltages at buses 1, 2, 3, and 4 are 1.002 pu, 0.990 pu, 0.964 pu and 0.951 pu, respectively. Subsequently (as in the Step-04 of the proposed algorithm), voltage control is moved to the alert state. According to Step-05, the sensitivity matrix, $S_M$ is derived. Next, the sequence of VAr support devices (i.e. LG units and CB) which are going to be utilized for coordinated VAr support is determined as in Step-06. According to Step-07, the new VAr reference values for selected VAr support devices are derived and they are 10.200 MVAR, 1.900 MVAR, 0.300 MVAR and 25 MVAR, respectively in the order of LG2, LG3, LG4 and CB. In this case, the voltages at buses 1, 2, 3, and 4 are 1.007 pu, 0.998 pu, 0.988 pu and 0.985 pu, respectively. The associated describing function analysis and eigen values are shown in Fig. 14 and Table IV, respectively. Finally, the updated VAr reference values are assigned for the local controllers of LG units and CB, and the OLTC local controllers are enabled according to Step-08 and Step-09, respectively of the proposed algorithm.

According to the above analyses and simulation studies, it is very clear that the proposed methodology of mitigating OLTC limit cycles is applicable to any network, after carefully simplifying and modeling the system and the control logic, compatible to implement the proposed algorithm.


