Voltage unbalance emission assessment in interconnected power systems

Jayatunga Vitharanage Upuli Priyadarshani Jayatunga
*University of Wollongong*, jvupj453@uowmail.edu.au

Sarath Perera
*University of Wollongong*, sarath@uow.edu.au

Philip Ciufo
*University of Wollongong*, ciufo@uow.edu.au

Ashish Agalgaonkar
*University of Wollongong*, ashish@uow.edu.au

Follow this and additional works at: [https://ro.uow.edu.au/eispapers](https://ro.uow.edu.au/eispapers)

Part of the Engineering Commons, and the Science and Technology Studies Commons
Voltage unbalance emission assessment in interconnected power systems

Abstract
The International Electrotechnical Commission (IEC) Technical Report IEC/TR 61000-3-13:2008 considers voltage unbalance (VU) emission assessment as a key aspect of the VU management of power systems. Compliance assessment of unbalanced installations at the post connection stage is essential to ensure that the limits set by the IEC VU emission allocation methodology in the pre-connection stage are met. Although VU is known to be caused by load asymmetries and inherent network asymmetries, locating all VU emission sources is not a straightforward process, especially in a network with interconnections. Such assessment methodologies should ensure that the contributions from various sources of unbalance to the total VU emission are determined using data which is not overly demanding. This paper presents deterministic methodologies which can be used to assess constituent components of post-connection VU level at the point of evaluation in an interconnected network utilising post connection voltage/current measurements and known system parameters. The theoretical bases are developed to cover different load types including induction motors. Emission assessment outcomes of different study systems obtained by employing the proposed methodologies are verified using unbalanced load flow analysis.

Disciplines
Engineering | Science and Technology Studies

Publication Details

This journal article is available at Research Online: https://ro.uow.edu.au/eispapers/2559
Voltage Unbalance Emission Assessment in Radial Power Systems

U. Jayatunga, Student Member, IEEE, S. Perera, Member, IEEE, and P. Ciufo, Senior Member, IEEE

Abstract—Voltage unbalance (VU) emission assessment is an integral part in the VU management process where loads are allocated a portion of the unbalance absorption capacity of the power system. The International Electrotechnical Commission Report IEC/TR 61000-3-13:2008 prescribes a VU emission allocation methodology establishing the fact that the VU can arise at the point of common connection (PCC) due to both upstream network unbalance and load unbalance. Although this is the case for emission allocation, approaches for post connection emission assessment do not exist except for cases where the load is the only contributor to the VU at the PCC. Such assessment methods require separation of the post connection VU emission level into its constituent parts. In developing suitable methodologies for this purpose, the pre- and post-connection data requirements need to be given due consideration to ensure that such data can be easily established. This paper presents systematic, theoretical bases which can be used to assess the individual VU emission contributions made by the upstream source, asymmetrical line and the load for a radial power system. The methodology covers different load configurations including induction motors. Assessments obtained employing the theoretical bases on the study system were verified by using unbalanced load flow analysis in MATLAB and using DigSILENT PowerFactory software.

Index Terms—power quality, voltage unbalance, current unbalance, voltage unbalance emission allocation, voltage unbalance emission assessment, system inherent asymmetry, load asymmetry

I. INTRODUCTION

VOLTAGE unbalance management in power systems requires approaches to allocate and assess VU emission levels in a systematic manner. In this regard, the IEC Technical report IEC/TR 61000-3-13:2008 [1] provides guiding principles to system operators and owners to determine the connection requirements of unbalanced installations to medium voltage (MV), high voltage (HV) and extra high voltage (EHV) public power systems. The philosophy of this report is similar to those of the counterpart IEC recommendations for harmonics (IEC 61000-3-6) [2] and flicker (IEC 61000-3-7) [3] allocation. Co-ordination of VU emission levels between different voltage levels of the power system are prescribed based on planning limits as reference values. The global emission allowance is then derived through a general summation law considering upstream emission contribution through transfer coefficients. The \( k_{uE} \) factor approach is used to account for the unbalance that arises at the PCC caused by upstream supply system asymmetry, when apportioning the global emission allowance to unbalanced installations. A range of values for the \( k_{uE} \) factor is given in [1] from which a suitable value can be selected for a system with some qualitatively characteristics.

Most of the available literature on voltage unbalance [4], [5] discuss its causes and effects and present related standards, definitions and mitigation techniques. Assessment of VU emission at the PCC with the help of the sequence energies is discussed in [6] and prediction of VU emission given by loads through unbalanced fundamental power is presented in [7]. None of the above work discuss or give methodologies that can be used to separate the individual emission contributions made by different sources of unbalance.

The work presented in [8] provides insights in relation to the approaches and concepts given in [1], including new methodologies to evaluate global VU emission due to load and line asymmetries. Although the unbalance arises due to unbalanced installations and inherent system asymmetries, determination of the level of contributions to the total VU emission at the point of evaluation (POE) is not a straightforward procedure due to the complex interactions that can take place between numerous sources, particularly in an interconnected network environment [9], [10]. The contribution of individual emission levels to the overall VU measured at the POE is often required with only knowledge of the pre- and post-connection voltage and current measurements in combination with some relatively easy to establish parameters.

In this regard, some preliminary work exists on VU emission assessment in C4.109 report [11] produced by the CI- GRE/CIRED joint working group on emission assessment techniques. The same work summarised in [12] presents the \( k_{uE} \) factor in a more generalized manner. However, this formulation does not consider the load type dependency or sensitivity of the \( k_{uE} \) factor to other system conditions. Thus, the proposed study makes a sufficiently rigorous and comprehensive fresh approach for compliance assessment providing further improvements to IEC work on VU management.

This paper presents deterministic approaches for the evaluation of individual VU emission contributions made by different sources of unbalance at the POE for a radial power system. The theoretical basis is developed to quantify the individual contributions made by upstream source unbalance, load asymmetry and line asymmetry at the POE for different load...
configurations. The mathematical models are verified using a 3-phase unbalanced load flow programs.

The paper is organised as follows: the general concepts associated with VU emission are described in Section II. The proposed mathematical modelling for the separation of different VU emission contributors is presented in Section III. Verification of the proposed methodology together with test system specifications are included in Section IV. Conclusions are given in Section V.

II. GENERAL CONCEPTS OF VU EMISSION [11]

According to IEC/TR 61000-3-13:2008 [1], the unbalance emission caused by an installation is the magnitude of the VU factor ($VUF = \frac{|U2|}{U1}$) which the installation under consideration gives rise to at the POE. In many practical situations, the zero sequence unbalance is generally ignored, considering its insignificance compared to negative sequence unbalance from an equipment impact perspective and/or because of three-wire situations.

The sequence voltages at the POE can be described as a function of the sequence currents drawn by the connected installation and the upstream network (e.g. transmission line) sequence impedance matrix $Z_{012,i}$. Thus, the negative sequence voltage at the POE ($U2$) can be expressed as in (1):

$$U_2 = U_{2,oc} - (Z_{20,i}I_0 + Z_{21,i}I_1 + Z_{22,i}I_2) \quad (1)$$

where $U_{2,oc}$ is the open circuit negative sequence voltage which can be obtained from pre-connection voltage measurement ($U_{2,pre-connection}$) at the POE. $U_2$ can be obtained from the post-connection voltage ($U_{2,post-connection}$) at the POE.

The following comments are applicable to the three impedance voltage drop terms in (1):

- $Z_{20,i}$ is relatively small and $I_0 = 0$ in many situations and hence $Z_{20,i}I_0$ can be ignored.
- $Z_{21,i}I_1$ is the negative sequence voltage that arises as a result of the asymmetrical upstream network itself and can be defined as the system inherent unbalance ($U_{2,pre-line}$). This term can be of significance since $I_1$ is usually large. $Z_{21,i}$ exists only if the upstream network is asymmetrical.
- $Z_{22,i}I_2$ can be of significance if there is an unbalance current due to load unbalance (assuming that there are no contributions to $I_2$ from upstream unbalance and asymmetrical upstream impedances). Therefore, this term is the negative sequence voltage which arise as a result of load unbalance and is defined as the load unbalance emission ($U_{2,load}$).

Hence, ignoring the term $Z_{20,i}I_0$, $U_2$ can be re-expressed as shown in (2):

$$U_2 = U_{2,oc} - (Z_{21,i}I_1 + Z_{22,i}I_2) = U_{2,oc} + U_{2,i} \quad (2)$$

where $U_{2,i}$ is the VU emission resulted from the connection of installation which consists of asymmetrical load and line contributions.

The approach given in CIGRE/CIRED report on emission assessment techniques [11] is to evaluate unbalance emission levels based on pre-connection and post-connection measurements at the POE. Having suitable measurement results with phase angle information (pre- and post-connection), the total emission $U_{2,i}$ can naturally be established as given by (3).

$$U_{2,i} = U_{2,post-connection} - U_{2,pre-connection} \quad (3)$$

Accordingly, the emission level that arises as a result of a particular installation can lead to an increase or a decrease of the resultant unbalance level at the POE. At the post-connection of the installation, if the net unbalance level decreases, no emission assessment has to be carried out in relation to the particular installation. Conversely, if the net unbalance increases, the fraction of the emission level which the installation is responsible for ($U_{2,i}(load)$) has to be evaluated.

So, challenge lies in the decomposition of $U_2$ to determine the individual contributions made by different sources of unbalance in the network (i.e., background unbalance voltage, asymmetrical supply network impedances, and the installation in question) for which a comprehensive methodology or methodologies do not exist.

In the straightforward case where the VU at the POE arises only as a result of installation symmetry, $U_{2,i}(load)$, can be established using the well known result employing the quantities of current unbalance factor ($CUF$ - i.e., the ratio of negative sequence to positive sequence current), load VA loading level ($S_i$) and the short circuit capacity ($S_{sc}$) as given by (4):

$$\frac{U_{2,i}(load)}{U_1} = \frac{S_i}{S_{sc}} |CUF| \quad (4)$$

Further to (4), the VU emission that arises at the POE due to an asymmetrical upstream network (or line) asymmetry ($U_{2,i}(line)$) can be estimated using:

$$\frac{U_{2,i}(line)}{U_1} = \frac{S_i}{S_{sc}} \frac{Z_{12}}{Z_1} \quad (5)$$

where, $Z_{12}$ is the positive-sequence negative-sequence coupling impedance of the upstream network, and $Z_i$ is the positive-sequence impedance of the upstream network.

The specific results given by (4) and (5) can be used individually to determine the contributions to the VU at the POE made by an installation or the network. However, they are not sufficiently comprehensive to be used with pre- and post-connection measurements to separate the VU at the POE into constituent components. Further, the impact of the upstream source unbalance\(^1\) on the POE is not accounted for by these results. Hence, it can be noted that a holistic approach and fresh formulations are required to separate the VU measurements at the POE into its constituent components, which forms the thrust of this paper. The specific results given by (4) and (5) can be shown to be intrinsically built into the mathematical formulations that will be presented in this paper.

\(^1\)Can be considered as a Thévenin equivalent unbalance voltage.
III. EVALUATION OF INDIVIDUAL UNBALANCE EMISSION CONTRIBUTIONS AT THE POE: THEORETICAL BASES

A deterministic approach is proposed which forms the bases for evaluation of the individual contributions made by the installation asymmetry, upstream network asymmetry and the upstream source unbalance on the total VU emission at the POE. Different load types are considered in developing this unbalance emission evaluation procedure and the corresponding results are verified by employing a 3-phase unbalanced load flow analysis and through other simulations.

The linearity property of negative sequence variables [1], [8] is employed in establishing the new methodology for the separation of different VU contributors. That is, the resultant negative sequence voltage at the POE which arises as a result of the interaction of various sources of unbalance, is equal to the phasor summation of the negative sequence voltage components caused by individual sources of unbalance at the POE.

The theoretical bases of the VU separation methodology is developed considering the radial power system as shown in Fig. 1, in reference to which the following points are to be noted:

- The power system is assumed to operate under sinusoidal steady state conditions.
- The POE is considered to be dedicated only to the unbalanced installation under consideration.
- Presence of unbalance at the POE is caused by load asymmetry, network asymmetry and unbalance in the source (background unbalance). Therefore all power system elements (source, load and network) are analysed in a generalised manner noting that all of these components can contribute to the total unbalance emission at the POE.
- Zero sequence behaviour is ignored assuming three wire systems.
- Theoretical bases are developed in terms of complex voltage/current unbalance factors at the POE. Thus, it is assumed that all voltage/current measurements in either pre- or post-connection stages at the POE are obtained as synchronised phasor quantities.
- Installation (load) is represented as a coupled impedance matrix in the case of passive loads (coupling impedances are present when converting unequal decoupled phase impedances into sequence domain) and as a decoupled impedance matrix in the case of induction motors in the sequence domain.

A. Separation of VU emission contributors: passive loads

1) Constant Impedance Loads: Referring to the radial power system shown in Fig. 1, positive and negative sequence voltages at the sending end (i.e., the source end) \((U_{1,send} \text{ and } U_{2,send})\) can be written as given below:

\[
U_{1,send} = U_{1,rec} + (Z_{11,t}I_1 + Z_{12,t}I_2) \tag{6}
\]

\[
U_{2,send} = U_{2,rec} + (Z_{21,t}I_1 + Z_{22,t}I_2) \tag{7}
\]

\(^{2}\)As stated in Section II, IEC/TR 61000-3-13 considers that zero sequence behaviour can be controlled through system design and maintenance.

![Fig. 1. Radial power system](image)

where \(U_{1,rec} \text{ and } U_{2,rec}\) are positive and negative sequence voltages at the POE respectively. \(I_1 \text{ and } I_2\) are positive and negative sequence currents flowing in the system respectively. \(Z_{xy,t}\) is sequence impedance of the transmission line\(^{3}\).

\(I_1 \text{ and } I_2\) can be found in terms of load parameters as given in (8) and (9). Detailed derivations of \(I_1\) and \(I_2\) are given in Appendix A.

\[
I_1 = \frac{U_{1,rec}}{Z_{11,rec}} \tag{8}
\]

\[
I_2 = \frac{Z_{21,rec}U_{1,rec} + U_{2,rec}}{Z_{11,rec}Z_{22,rec} + Z_{22,rec}} \tag{9}
\]

where, \(Z_{xy,rec}\) is the sequence impedance seen at the POE (equal to load impedance).

Case I: The upstream source is balanced. In this case, as \(U_{2,send} = 0\) in (7), the unbalance at the POE arises as a result of load asymmetry and line asymmetry only. Substitution of \(I_1 \text{ and } I_2\) from (8) and (9) into (7) results in the VU factor at the POE \((VUF_{POE})\) as follows.

\[
VUF_{POE} = \frac{Z_{22,rec}Z_{21,rec} - Z_{21,t}Z_{22,rec}}{(Z_{22,rec} + Z_{22,t})Z_{11,rec}} \tag{10}
\]

Employing the fact that the negative sequence impedance of the network \((Z_{22})\) is equal to its positive sequence impedance \((Z_{11})\), \(VUF_{POE}\) can be rewritten as shown by (11).

\[
VUF_{POE} = \frac{Z_{22,rec}Z_{21,rec}}{(Z_{11,send}Z_{11,rec}) - Z_{21,t}} \tag{11}
\]

where \(Z_{11,send} = Z_{11,rec}+Z_{21,t}\). By defining voltage regulation of the line, \(V_{reg-line}\), as the ratio of positive sequence voltage drop in the network (line) to positive sequence voltage at the receiving end as given by (12), the expression for \(VUF_{POE}\) can be further modified as shown in (13).

\[
V_{reg-line} = \frac{U_{1,t}}{U_{1,rec}} = \frac{Z_{11,t}}{Z_{11,rec}} = \frac{Z_{22,t}}{Z_{11,rec}} \tag{12}
\]

\[
VUF_{POE} = \frac{Z_{21,rec}}{Z_{11,rec} (1 + V_{reg-line})} - \frac{Z_{21,t}}{V_{reg-line}} \tag{13}
\]

Referring to Eqn. (13), if the load is symmetrical (i.e., the coupling impedance \((Z_{21,rec} = 0)\), the unbalance at the POE arises as a result of the network (line) only which can be assessed using the factor \(\frac{Z_{21,t}}{Z_{11,rec} (1 + V_{reg-line})}\). Conversely, for a symmetrical network, the positive-sequence negative-sequence

\(^{3}\)x and y are replaced by 1 and 2 which stand for positive sequence and negative sequence respectively.
coupling impedance \( Z_{21,t} = 0 \) and the VU that arises at the POE due to load asymmetry can be established using the factor \( \frac{Z_{21,rec}}{Z_{11,rec}(1 + V_{reg-line})} \).

For the case where the installation is symmetrical but the supply network is asymmetrical (i.e., \( Z_{21,rec} = 0, Z_{21,t} \neq 0 \)), it may be demonstrated that the VU at the POE given by the rigorous outcome of (13) is similar to the result given by (5).

Case II: The upstream source is unbalanced \( U_{2,send} \neq 0 \). In this case, the source, installation and the network (line) can together make contributions to the total VU emission at the POE. So, When \( U_{2,send} \neq 0 \) in (7) the VUF\(_{POE}\) can be found following the same steps as described in Case I and shown to be depicted by three components as represented by (14):

\[
\text{VUF}_{\text{POE}} = \text{VUF}_{\text{source}} + \frac{Z_{21,rec}}{Z_{11,rec}(1 + V_{reg-line})} \frac{V_{reg-line}}{V_{reg-line}} - \frac{Z_{21,t}}{Z_{11,t}(1 + V_{reg-line})} \ (14)
\]

Eqn. (14) shows that the source unbalance transfers to the POE with no attenuation, whereas the network (line) and installation contributions to the POE are similar to those in Case I.

\[
U_{2,\text{source}} = \text{VUF}_{\text{source}}
\]

\[
U_{2,\text{t(load)}} = \text{VUF}_{\text{POE-load}} = \frac{Z_{21,rec}}{Z_{11,rec}(1 + V_{reg-line})} \frac{V_{reg-line}}{V_{reg-line}} - \frac{Z_{21,t}}{Z_{11,t}(1 + V_{reg-line})}
\]

\[
U_{2,\text{t(line)}} = \text{VUF}_{\text{POE-line}} = \frac{Z_{21,rec}}{Z_{11,rec}(1 + V_{reg-line})} \frac{V_{reg-line}}{V_{reg-line}} - \frac{Z_{21,t}}{Z_{11,t}(1 + V_{reg-line})}
\]

2) Constant Power Loads: Eqn. (14) can be further modified, in terms of voltage and current unbalance factors, by eliminating impedance terms associated with the installation (i.e., \( Z_{21,rec} \) and \( Z_{11,rec} \)) to the form given in (15) (see Appendix B for the proof). This process will facilitate the evaluation of VU emission related to loads of which impedance details are not available.

The VU emission that arises at the POE as a result of load asymmetry only is:

\[
\text{VUF}_{\text{load}} = (\text{VUF}_{\text{POE}} - \text{CUF}) \frac{V_{reg-line}}{V_{reg-line}} \ (15)
\]

Eqn. (15) can be shown to be a rigorous and a general form of the result given by (4). Incorporating (15), the VU at the POE for constant power loads can be re-expressed as shown in (16):

\[
\text{VUF}_{\text{POE}} = \text{VUF}_{\text{source}} + (\text{VUF}_{\text{POE}} - \text{CUF}) \frac{V_{reg-line}}{V_{reg-line}} - \frac{Z_{21,t}}{Z_{11,t}(1 + V_{reg-line})} \ (16)
\]

3) Constant Current Loads: Although constant current loads draw currents of equal magnitude in the three supply phases regardless of the prevailing voltage balance/unbalance conditions, negative sequence current associated with load current can be significant as a result of unequal phase angle displacements of line current. This means, asymmetrical constant current load behaviour can be considered to the similar manner how the constant power load was previously examined for VU emission assessment.

B. Separation of VU emission contributors: induction motors (IM)

Generally, three phase induction motors do not possess any inherent unbalance other than the fact that their operation is affected by the supply source unbalance. Further three phase induction motors are known to improve (reduce or compensate) pre-existing VU level at a PCC. Therefore, this specific case is aimed at quantifying the VU emission compensation provided at the POE by three phase induction motors.

The passive load in Fig. 1 is replaced with a 3-phase induction motor which can be represented by three decoupled impedances in the sequence domain. Therefore, expressions for positive and negative sequence motor currents \( (I_{1,m} \text{ and } I_{2,m}) \) respectively can be established by:

\[
I_{1,m} = \frac{U_{1,rec}}{Z_{1,m}} \ (17)
\]

\[
I_{2,m} = \frac{U_{2,rec}}{Z_{2,m}} \ (18)
\]

where \( Z_{1,m} \) and \( Z_{2,m} \) are the positive and negative sequence impedances, respectively, of the motor which can be established from the motor equivalent circuit.

Substitution of the sequence currents given by (17) and (18) in (6) and (7) gives the total VU at the POE as given by (19):

\[
\text{VUF}_{\text{POE}} = \left( \frac{Z_{2,m}}{Z_{1,m}} \right) \left( \frac{Z_{1,m} + Z_{11,t}}{Z_{2,m} + Z_{22,t}} \right) \text{VUF}_{\text{source}} - \left( \frac{Z_{2,m}}{Z_{1,m}} \right) \frac{Z_{21,t}}{Z_{22,t} + Z_{2,m}} \ (19)
\]

Using (19), the improvement made by the connection of three phase induction motor on an already unbalanced supply system can be noted by considering the special case where the transmission line is symmetrical (i.e., \( Z_{21,t} = 0 \)). For this case, the VU emission at the POE is the source VU level \( \text{VUF}_{\text{source}} \), scaled by a factor \( \left( \frac{Z_{2,m}}{Z_{1,m}} \right) \left( \frac{Z_{1,m} + Z_{11,t}}{Z_{2,m} + Z_{22,t}} \right) \), having a magnitude less than unity, which incorporates positive and negative sequence impedances of the line and the motor.

From (19) the influence made by the asymmetrical supply network (line) on the total unbalance is given by the factor \( \left( \frac{Z_{2,m}}{Z_{1,m}} \right) \left( \frac{Z_{21,t}}{Z_{22,t} + Z_{2,m}} \right) \).

C. Separation of VU emission contributors: mix of passive and induction motor loads

When the radial system supplies a mix load, the total line current can be found as the summation of the individual positive \( (I_{1,PL} \text{ and } I_{1,m}) \) and negative sequence \( (I_{2,PL} \text{ and } I_{2,m}) \) currents drawn by the passive and induction motor loads. Accordingly, the negative sequence voltage at the POE given in (7) can be modified and the VUF at the POE that arises as a result of the mixed load can be shown with appropriate substitutions to take the form of (20):

\[
\text{VUF}_{\text{POE}} = A \text{VUF}_{\text{source}} + B + C \ (20)
\]
where the factors A, B and C are given by the following expressions:

\[ A = \frac{1 + \frac{Z_{11,t}}{Z_{11,rec}}}{1 + \frac{Z_{22,t}}{Z_{22,reg}} + \frac{Z_{22,t}}{Z_{22,PL}}} \]  

(21)

\[ B = \frac{Z_{21,PL} + Z_{22,t}}{Z_{22,reg} + Z_{22,PL} + Z_{22,t}} \]  

(22)

\[ C = -\frac{Z_{21,t}}{1 + \frac{Z_{22,t}}{Z_{22,reg}} + \frac{Z_{22,t}}{Z_{22,PL}}} \]  

(23)

Referring to (22), if the passive load is symmetrical, the positive sequence negative sequence coupling impedance, \( Z_{21,PL} \) is zero and hence \( B = 0 \). This means, the factor \( B \) can be considered as the VU emission contribution made by the load asymmetry.

Referring to (23), if the transmission line is transposed, the positive sequence negative sequence coupling impedance of the line, \( Z_{21,t} \) is zero. Accordingly, \( C = 0 \) and hence the factor \( C \) can be considered as the VU emission contribution that arises as a result of the line asymmetry.

If both the passive load and the transmission line are symmetrical (i.e., \( B = C = 0 \)), then the VUF at the POE is given by \( A \) (\( VUF_{source} \)). Thus, the factor \( A \) given by (21) (of which the magnitude is less than unity) can be interpreted as the upstream or source VU modification factor that needs to be considered in the presence of an induction motor load.

These three factors, \( A \), \( B \) and \( C \), can be re-expressed in terms of known system parameters by introducing the voltage regulation of the line (\( V_{reg-line} \)) and the current unbalance factor of the passive load (\( CUF_{PL} \)).

Therefore, the individual VU emission contributions made by the induction motor, \( U_{2,i(IIM)} \), the passive load \( U_{2,i(PL)} \), and the upstream network \( U_{2,i(line)} \), at the POE can be summarised as shown in (24), (25) and (26).

IV. VERIFICATION OF THE METHODOLOGY

A. Test network and the loads

A radial network based on a 12.47 kV test system given in [8] was used to determine VU emission contributions made by the different sources. This system consists of the upstream source, untransposed network (line), and the load including passive load types (constant impedance, constant current or constant power loads), three-phase induction motor loads and a mix of these load types. The passive load types were modelled employing the exponential load models [13] as given by (27):

\[ P = P_0 \left( \frac{V}{V_0} \right)^\alpha \]  

and \( Q = Q_0 \left( \frac{V}{V_0} \right)^\beta \)  

(27)

where \( P \), \( Q \) are the active and reactive power consumed by the load at voltage \( V \). The terms \( P_0, Q_0 \) represent the rated active and reactive powers, respectively, at the nominal voltage \( V_0 \). The exponents \( \alpha \) and \( \beta \) are the voltage coefficients of the active and reactive powers, respectively. For constant power loads, \( \alpha = 0 \), for constant current loads, \( \alpha, \beta = 1 \) and for constant impedance loads, \( \alpha, \beta = 2 \).

The details of the test system are as follows.

- System details: 12.47 kV, 60 Hz, three wire
- 12.47 kV untransposed line:
  - Tower construction details: 1.143 m flat and horizontal
  - Conductor data:
    - Geometric mean radius = 7.7724 mm
    - AC resistance = 0.19014 \( \Omega \)/km
    - Earth resistivity = 100 \( \Omega \) m
  - Calculated line impedance matrix (\( [Z_{abc}] \)/km)
    - 0.2494 + j0.8748 0.0592 + j0.4985 0.0592 + j0.4985
    - 0.0592 + j0.4985 0.2494 + j0.8748 0.0592 + j0.4985
    - 0.0592 + j0.4985 0.0592 + j0.4985 0.2494 + j0.8748

- Load:
  - Passive load: A 10 MVA/1 MVA three phase load with lagging power factors of 0.85, 0.90 and 0.50 in phases a, b and c respectively.
  - Induction motor load: A 2250 hp, 2.3 kV, 3 phase induction motor of which the equivalent circuit parameters are given in [14].

An unbalanced load flow program written in MATLAB in conjunction with unbalanced load flow analysis using DigsILENT were used to verify the outcomes of the mathematical formulations. For brevity, the VU separation outcomes presented in the following subsections cover only a limited number of cases addressed by the formulations:

- passive load - constant power type,
- induction motor load, and
- mix of passive and induction motor load.

B. Passive loads: Constant Power loads

The radial power system with a 3.2187 km line (voltage regulation of the line (\( |V_{reg-line}| \)) - 9.6%) supplying 10 MVA single phase constant power type load set was simulated with the unbalanced load flow program in MATLAB. The VU emission separation outcomes (the general application of Eqn (16)) is illustrated in Fig. 2 using phasor diagrams for four different upstream source unbalance levels. The data (generated from unbalanced load flow program) which is used to produce phasor diagrams is given in Appendix C.

The phasor diagrams of Fig. 2 show VU emission in terms of individual VU factors (indicated as a percentage alongside the individual phasors) that arise at the POE as a result of the different sources of unbalance. In these diagrams:

- \( VUF_{source} \) represents the VU emission at the upstream or source (i.e., pre-connection VU emission at the POE) which is zero for case (a) but finite for cases (b)-(d)
- \( VUF_{POE,loadflow} \) is the post-connection resultant VU emission at the POE obtained using unbalanced load flow studies
- \( VUF_{POE,cal} \) is the post-connection resultant VU emission at the POE evaluated using the formulation
- phasor \( VUF_{POE,load} \) represents the VU emission at the POE caused by the load asymmetry evaluated using the formulation
- \( VUF_{POE,line} \) is the VU emission at the POE caused by the line asymmetry evaluated using the formulation
The upstream source voltage unbalance modified by the induction motor (A)

\[ U_{2,i(IM)} = \left( \frac{Z_{1,m}Z_{2,m} (1 + V_{\text{reg-line}})}{Z_{1,m}Z_{2,m} (1 + V_{\text{reg-line}}) + Z_{22,t} (Z_{1,m} - Z_{2,m})} \right) V_{\text{UFsource}} \]  \hspace{1cm} (24)

Asymmetrical passive load contribution (B)

\[ U_{2,i(PL)} = (V_{\text{UFPOE}} - C_{\text{UFPL}}) \left( \frac{Z_{1,m}Z_{2,m} (V_{\text{reg-line}} - \frac{Z_{22,t}}{Z_{1,m}})}{Z_{1,m}Z_{2,m} (1 + V_{\text{reg-line}}) + Z_{22,t} (Z_{1,m} - Z_{2,m})} \right) \]  \hspace{1cm} (25)

Asymmetrical line contribution (C)

\[ U_{2,i(\text{line})} = -\frac{Z_{21,t}}{Z_{11,t}} \left( \frac{Z_{1,m}Z_{2,m}V_{\text{reg-line}}}{Z_{1,m}Z_{2,m} (1 + V_{\text{reg-line}}) + Z_{22,t} (Z_{1,m} - Z_{2,m})} \right) \]  \hspace{1cm} (26)

The reader should note that the \( V_{\text{UFPOE,cal}} \) is represented as the reference phasor with a phase angle of zero degrees although this angle may take on any value. Accordingly, all other vectors are represented with respect to the above reference phasor.

The nearly coincident resultant VU emission vectors with nearly similar magnitudes representing the VU at the POE established using the formulation and through load flow analysis, demonstrate that the mathematical formulation is sufficiently accurate. In addition, the following further conclusions can be drawn:

- For case (a), the cancellation of VU emissions made by load (1.15%) and line (0.78%) are seen to help lower the VU emission at the POE to (0.68%).
- The magnitudes of VU contributions made by the network are seen to be similar (0.78%, 0.78%, 0.76%, 0.77%) in all four cases although they exhibit different phase shifts with respect to the reference phasor. The similarity in magnitude can be attributed to the fact that the passive load \( \text{VA} \) is nearly the same for all cases.
- The upstream source unbalance is seen to have some degree of influence on the VU emission by the load (compare 1.15% with 1.17%, 1.06% and 1.4%).

The resultant VU emission at the POE is seen to be significantly influenced not only by the magnitudes of the individual contributions but also by their phasor orientations. In other words, significant cancellations or strengthening (amplifications) may be observed between load pre-connection and load post-connection stages.

C. Induction motor load

The 12.47 kV radial power system with a 5 km line (\(|V_{\text{reg-line}}| = 5.5\%\)) supplying a 3-phase induction motor load (see Section IV-A) was modelled using DiGSI\text{L}ENT to validate the proposed VU emission separation methodology. A three-phase two-winding Yg-Yg connected transformer model was used as the motor service transformer with a voltage ratio: 12.47/2.3 kV and a leakage reactance of 5% pu.

The separation of the VU emission (the application of (19)) at the POE is shown in Fig. 3 using phasor diagrams for four different upstream VU levels. The general comments applicable for the phasor diagrams relevant to the case of passive loads is applicable here as well. Further, the resultant VU emission vectors representing the VU at the POE established using the formulation and through simulation are in close agreement thus proving the validity of the formulation.

Based on the phasor diagrams of Fig. 3, following further conclusions can be drawn:

- The improvement in the VU emission at the POE that usually takes place when 3-phase induction motors are connected is clearly evident which can be seen by examining the magnitudes of \( V_{\text{UFsource}} \) and \( V_{\text{UFPOE,IM}} \) for cases (b)-(d) (compare 0.58%, 1.16%, 2.33% with
0.47%, 0.94%, 1.88% respectively).

- The scaling factor associated with this reduction (see (18)) is not seen to introduce a significant phase shift between the pre- and post-connected VU emission vectors $VUF_{source}$ and $VUF_{POE,IM}$.

**Fig. 3.** Separation of VU emission at POE with different source unbalance levels: induction motor load, untransposed line. (a). $VUF_{source} = 0\%$ (b). $VUF_{source} = 0.58\%$ (c). $VUF_{source} = 1.16\%$ (d). $VUF_{source} = 2.33\%$

**D. Mix of passive load and induction motor load**

The 12.47 kV radial power system consisting of 5 km untransposed line supplying a mixed load is considered here. 2.3 kV, 2250 hp 3-phase induction motor (connected via a step down service transformer - 12.47/2.3 kV, 8 MVA, leakage reactance: 5% pu, connection: Yg-Yg) and 1 MVA passive load set (see Section IV-A) was modelled as a mixed load using DIgSILENT to validate the proposed VU emission separation methodology (see (20)).

The phasor diagram representation showing the individual emission contributions made by different unbalance sources is given in Fig. 4. These contributors and the corresponding emission vectors are:

- network (line), $VUF_{POE, line}$
- passive load, $VUF_{POE, pl}$
- source, $VUF_{source}$
- induction motor, $VUF_{POE, IM}$

As in the cases of pure passive load and induction motor load, the resultant VU emission vectors at the POE established using the formulation and through simulation are in close agreement thus proving the validity of the formulation.

The general observation is that, although the unbalance emission at the POE is improved in the presence of the induction motor, the emission contribution that arise as a result of the passive load is relatively more significant and hence the resultant unbalance emission at the POE is greater than the pre-connection unbalance emission ($VUF_{source}$) at the POE.

**Fig. 4.** Separation of VU emission at POE with different source unbalance levels: mixed load consisting a passive and an induction motor load, untransposed line. (a). $VUF_{source} = 0\%$ (b). $VUF_{source} = 0.58\%$ (c). $VUF_{source} = 1.16\%$ (d). $VUF_{source} = 2.33\%$

**V. CONCLUSIONS**

This paper presented complex unbalance factor based formulations which can be used to evaluate the VU emission contributions made by different sources of unbalance at a POE utilising snap-shot measurements of the power system. Several major conclusions can be drawn from the work presented:

- The asymmetrical line contribution to the total VU emission at the POE has been evaluated as a decoupled quantity using the characteristic sequence impedance terms of the transmission line.
- A more generalised expression has been derived for the VU emission due to load (passive loads) asymmetry as given by the factor $((VUF_{POE} - CUF) \frac{V_{pre-line}}{V_{reg-line}})$ which utilise only the pre-connection and post-connection voltage/current measurements at the POE.
- The ability of induction motors to attenuate the VU at the POE has been confirmed through this study quantifying the improvement.
- The effect of upstream source unbalance propagated to the POE has been separated in terms of $VUF_{source}$.

Based on the proposed approach, an advanced power quality instrument may be configured to monitor and record voltage/current measurements and switching status of loads/lines continuously which enable the assessment of VU as a continuous time domain function to accommodate dynamic behaviour of the power system. Such a set up may provide further investigation opportunities on the statistical approaches given in IEC documents on voltage unbalance management.

**APPENDIX A**

**SEQUENCE CURRENTS IN THE RADIAL SYSTEM: PROOF OF**

(8) **AND** (9)

$U_{1,rec}$ and $U_{2,rec}$ at the POE can be expressed in terms of load current and load impedance ($Z_L = Z_{rec}$) as shown in
\[ U_{1,\text{rec}} = Z_{11,\text{rec}} I_1 + Z_{12,\text{rec}} I_2 \]  
\[ U_{2,\text{rec}} = Z_{21,\text{rec}} I_1 + Z_{22,\text{rec}} I_2 \]  
(A.1) and (A.2).

\[ I_1 \text{ can be derived approximating } U_{1,\text{rec}} = Z_{11,\text{rec}} I_1, \text{ since } \] 
the voltage drop of \( Z_{12,\text{rec}} I_2 \) is negligible compared to the contribution caused by the voltage drop given by \( Z_{11,\text{rec}} I_1 \) on \( U_{1,\text{rec}} \). Hence, the positive sequence current, \( I_1 \), can be established as:

\[ I_1 = \frac{U_{1,\text{rec}}}{Z_{11,\text{rec}}} \]  
(A.3)

From (A.2), the negative sequence current, \( I_2 \), flowing through the system can be expressed as:

\[ I_2 = \frac{U_{2,\text{rec}} - Z_{21,\text{rec}} I_1}{Z_{22,\text{rec}}} \]  
(A.4)

Replacing the positive sequence current \( I_1 \) in (A.4) with (A.3), the expression for \( I_2 \) can be further rearranged as follows.

\[ I_2 = \frac{Z_{21,\text{rec}} U_{1,\text{rec}}}{Z_{11,\text{rec}} Z_{22,\text{rec}}} + \frac{U_{2,\text{rec}}}{Z_{22,\text{rec}}} \]  
(A.5)

**APPENDIX B**

**VU EMISSION MADE BY LOAD ASYMMETRY: PROOF OF (15)**

The VU that arises as a result of load asymmetries \( (VUF_{\text{load}}) \) as given in (14) is:

\[ VUF_{\text{load}} = \frac{Z_{21,\text{rec}} V_{\text{reg-line}}}{Z_{11,\text{rec}} (1 + V_{\text{reg-line}})} \]

Rewriting the revised receiving end (load end) sequence voltages as shown in (B.1) and (B.2), the ratio of \( \frac{Z_{11,\text{rec}}}{Z_{21,\text{rec}}} \) can be calculated as shown in (B.3).

\[ U_{1,\text{rec}} = Z_{11,\text{rec}} I_1 \]  
(B.1)

\[ U_{2,\text{rec}} = Z_{21,\text{rec}} I_1 + Z_{22,\text{rec}} I_2 \]  
(B.2)

Then:

\[ Z_{11,\text{rec}} = \frac{U_{1,\text{rec}}}{I_1} \]

\[ Z_{21,\text{rec}} = \frac{U_{2,\text{rec}} - Z_{22,\text{rec}} I_2}{I_1} \]

\[ Z_{22,\text{rec}} = \frac{U_{2,\text{rec}} - Z_{21,\text{rec}} I_2}{U_{1,\text{rec}}} \]  
(B.3)

Utilising the fact that the positive sequence impedance is equal to the negative sequence impedance for all passive loads, Eqn. (B.3) can be rearranged as:

\[ \frac{Z_{21,\text{rec}}}{Z_{11,\text{rec}}} = VUF_{POE} - CUF \]  
(B.4)

where \( VUF_{POE} \) is the VU factor at the POE which is given by \( \frac{U_2}{I_2} \) and \( CUF \) is the current unbalance factor given by \( \frac{I_2}{I_1} \).

---

**REFERENCES**


---

APPENDIX C

**VOLTAGE UNBALANCE EMISSION DATA**

The data used to generate the phasor diagrams presented in Figs 2, 3 and 4 are presented in Tables I, II and III respectively.

Decoupled VU emission contributions \( (VUF_{\text{load}}, VUF_{\text{line}} \) and \( VUF_{\text{POE},LM} \) derived only through the proposed formulation are shown in the tables for brevity. But, It has been validated that the same values can be obtained through simulations considering specific cases of the simulated power system. That is, making one component unbalanced at a time, while keeping the rest of the system balanced, the VU level at the POE gives the contribution made by that individual unbalanced component.
TABLE I
VOLTAGE UNBALANCE EMISSION DATA USED TO GENERATE PHASOR DIAGRAMS FOR CONSTANT POWER LOAD

<table>
<thead>
<tr>
<th>#</th>
<th>Simulation results</th>
<th>Proposed formulation results</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Source unbalance (VUFsource) %</td>
<td>VU at POE (VUFPOE) %</td>
</tr>
<tr>
<td>a</td>
<td>0.12/0.1</td>
<td>0.72/0.95</td>
</tr>
<tr>
<td>b</td>
<td>0.58/290.7</td>
<td>1.29/27.8</td>
</tr>
<tr>
<td>c</td>
<td>1.6/120.0</td>
<td>1.72/210.9</td>
</tr>
<tr>
<td>d</td>
<td>2.08/243.9</td>
<td>3.00/299.2</td>
</tr>
</tbody>
</table>

TABLE II
VOLTAGE UNBALANCE EMISSION DATA USED TO GENERATE PHASOR DIAGRAMS FOR AN INDUCTION MOTOR LOAD

<table>
<thead>
<tr>
<th>#</th>
<th>Simulation results</th>
<th>Proposed formulation Results</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Source/ upstream unbalance (VUFsource) %</td>
<td>VU at POE (VUFPOE) %</td>
</tr>
<tr>
<td>a</td>
<td>0.12/0.1</td>
<td>0.17/2 – 168</td>
</tr>
<tr>
<td>b</td>
<td>0.58/29.8</td>
<td>0.32/45.8</td>
</tr>
<tr>
<td>c</td>
<td>1.6/29.9</td>
<td>0.79/29.8</td>
</tr>
<tr>
<td>d</td>
<td>2.33/29.3</td>
<td>1.72/25.8</td>
</tr>
</tbody>
</table>

TABLE III
VOLTAGE UNBALANCE EMISSION DATA USED TO GENERATE PHASOR DIAGRAMS FOR A MIXED LOAD

<table>
<thead>
<tr>
<th></th>
<th>Simulation results</th>
<th>Proposed formulation results</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Source/ upstream unbalance (VUFsource) %</td>
<td>VU at POE (VUFPOE) %</td>
</tr>
<tr>
<td>a</td>
<td>0.0/2.0</td>
<td>0.46/24.12</td>
</tr>
<tr>
<td>b</td>
<td>0.58/29.8</td>
<td>0.93/29.08</td>
</tr>
<tr>
<td>c</td>
<td>1.16/29.7</td>
<td>1.40/24.24</td>
</tr>
<tr>
<td>d</td>
<td>2.33/29.3</td>
<td>2.36/35.75</td>
</tr>
</tbody>
</table>

U. Jayatunga (S’2010) received the B.Sc.(Eng) degree in electrical power engineering and a M.Sc. degree in electrical engineering from the University of Moratuwa, Sri Lanka. Currently she is pursuing studies towards the Ph.D. degree at the University of Wollongong, Australia. Her research interests are in power quality and power system analysis.

S. Perera (M’1995) received the B.Sc.(Eng) degree in electrical power engineering from the University of Moratuwa, Sri Lanka, a M.Eng.Sc. degree from the University of New South Wales, Australia, and the Ph.D. degree from the University of Wollongong, Australia. He has been a lecturer at the University of Moratuwa, Sri Lanka. Currently he is an Associate Professor with the University of Wollongong. He is the Technical Director of the Endeavour Energy Power Quality and Reliability Centre at the University of Wollongong.

P. Ciuflo (SM’2007) graduated from the University of Wollongong with a B.E. (Hons) in Electrical Engineering in 1990 whilst also completing an Industry Cadetship. He obtained an M.E. (Hons) in Electrical Engineering in 1993. He joined the academic staff of the University after completing his Ph.D. in 2002. Dr Ciuflo has had various stints in industry as an electrical engineer and returned to academia in 2007. His research interests include Modelling and Analysis of Power Distribution Systems, Distribution Automation, Modelling and Analysis of AC Machines, Power System Harmonics and Power System Reliability.