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Efficient Secure Matrix Multiplication over LWE-Based Homomorphic Encryption

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Abstract
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EFFICIENT SECURE MATRIX MULTIPLICATION OVER LWE-BASED HOMOMORPHIC ENCRYPTION

Dung Hoang Duong — Pradeep Kumar Mishra — Masaya Yasuda

ABSTRACT. Homomorphic encryption enables various calculations while preserving the data confidentiality. In this paper, we apply the somewhat homomorphic encryption scheme proposed by Brakerski and Vaikuntanathan (CRYPTO 2011) to secure matrix multiplication between two matrices. To reduce both the ciphertext size and the computation cost, we propose a new method to pack a matrix into a single ciphertext so that it also enables efficient matrix multiplication over the packed ciphertexts. Our packing method generalizes Yasuda et al.’s methods (Security Comm. Networks 2015 and ACISP 2015), which are for secure inner product. We also implement our methods and give a comparison with previous packing methods.

1. Introduction

Homomorphic encryption is a form of encryption that can support meaningful operations on encrypted data (without decryption). This encryption has been expected to give a powerful tool for data protection in cloud computing. The concept of homomorphic encryption was first introduced by Rivest et al. in [7, 1978], and the first fully homomorphic encryption (FHE) scheme that supports arbitrary computations on encrypted data was constructed by Gentry [4]. However, currently known FHE schemes are yet impractical. In contrast, somewhat homomorphic encryption (SHE) schemes (e.g., the Boneh-Goh-Nissim (BGN) scheme [1], and the building block for an FHE scheme), which support only a limited number of additions and multiplications on encrypted data, have wider applications and take a lot of attention from various communities.
We use the SHE scheme proposed by Brakerski and Vaikuntanathan [3]. The security of the scheme relies on the computational hardness of the polynomial version of the learning with errors (LWE) problem, a simplified version of the ring-LWE assumption of [6]. Over the SHE scheme, Lauter et al. [5] proposed a method to pack an integer of large size into a single ciphertext so that it enables to efficiently compute secure sums and products over the integers. After that, Yasuda et al. [9] proposed a new packing method for secure multiple inner products, which can be applied to secure Hamming distance and pattern matching computations. While their packing method is efficient only for vectors with small size entries, they modified their method for large size entries [10]. Their modified method can be applied to secure statistical analysis such as covariance and correlation between two variables with integers of practical size.

In this paper, we propose several packing methods for secure matrix multiplication, using the idea of Yasuda et al.’s methods [9], [10]. More specifically, for matrices with binary entries, our method is based on [9], and for non-binary entries, it is based on [10]. Our main ingredient for packing a matrix $A$ is to deploy the entries of $A$ into a single polynomial by using the packing methods of [9], [10], and then encrypt the polynomial over the SHE scheme. Our matrix packing method requires only one homomorphic multiplication over our packed ciphertexts for secure matrix multiplication. In Section 2, we review the SHE scheme proposed by Brakerski and Vaikuntanathan. In Section 3, we present previous packing method over the SHE scheme. In Section 4, we present our packing methods for secure matrix multiplication. In Section 5, we give implementation results and discuss about a comparison with previous methods.

2. Preliminaries

In this section, we briefly recall the construction and the homomorphic correctness of the somewhat homomorphic encryption scheme proposed by Brakerski and Vaikuntanathan [3]. Let $n$ be a 2-power integer defining the base ring $R = \mathbb{Z}[x]/(x^n + 1)$. Let $q$ be a prime number with $q \equiv 1 \mod 2n$ defining the ring $R_q = R/qR = \mathbb{F}_q[x]/(x^n + 1)$, which gives the base ring of ciphertext space. According to [2], the condition $q \equiv 1 \mod 2n$ is not necessary for the construction and the security but for the efficiency of the scheme. Let $t$ be a positive integer with $t < q$ defining the plaintext space $R_t = R/tR$. Let $\sigma$ be the parameter for defining a discrete Gaussian error distribution $\chi = D_{\mathbb{Z},\sigma}$ (specifically, we select each entry in an $n$-dimensional vector by sampling from a Gaussian distribution $N(0, \sigma)$ and then round it to the nearest integer).
The security of the scheme (constructed in Section 2.1 below) relies on the following polynomial-LWE assumption, which is a simplified version of the ring-LWE assumption of Lyubashevsky, Peikert and Regev [6] (the following assumption is independent of the parameter $t$, see [3, Proposition 11] for details).

**Definition 1** (Polynomial-LWE). Given $(n, q, t, \sigma)$, the polynomial-LWE assumption is that it is infeasible to distinguish the following two distributions:

1. One samples $(a_i, b_i)$ uniformly from $(R_q)^2$.
2. One first draws $s \leftarrow \chi = D_{\mathbb{Z}_n, \sigma}$ uniformly and then samples $(a_i, b_i) \in (R_q)^2$ by sampling $a_i \leftarrow R_q$ uniformly, $e_i \leftarrow \chi$ and setting $b_i = a_i s + e_i$.

### 2.1. Somewhat homomorphic encryption scheme

We here present the construction of the public-key SHE scheme. Specifically, the below construction is a variant of [5, Section 3.2].

- **Key generation:** Choose an element $R \ni s \leftarrow \chi$, sample a uniformly random element $p_1 \in R_q$ and an error $R \ni e \leftarrow \chi$. Set $pk = (p_0, p_1)$ with $p_0 = -(p_1 s + t e)$ as the public key and $sk = s$ as the secret key.

- **Encryption:** For a plaintext $m \in R_t$, sample $R \ni u, f, g \leftarrow \chi$ and compute the fresh ciphertext

\[
Enc(m, pk) = (c_0, c_1) = (p_0 u + tg + m, p_1 u + tf) \in (R_q)^2,
\]

where the plaintext $m \in R_t$ is regarded as an element of $R_q$, since $t < q$.

- **Homomorphic operations:** Given two (fresh or operated) ciphertexts $ct = (c_0, c_1, \ldots, c_\xi)$ and $ct' = (c'_0, c'_1, \ldots, c'_\eta)$ (note that the length of a ciphertext increases by the homomorphic multiplication defined below). Then the homomorphic addition “$+$” is computed by component-wise addition

\[
ct + ct' = (c_0 + c'_0, \ldots, c_{\max(\xi, \eta)} + c'_{\max(\xi, \eta)}),
\]

by padding with zeros if $\xi \neq \eta$, and the homomorphic multiplication “$\ast$” is defined by $ct \ast ct' = (\tilde{c}_0, \ldots, \tilde{c}_{\xi+\eta})$ with

\[
\sum_{i=0}^{\xi+\eta} \tilde{c}_i z^i = \left( \sum_{i=0}^{\xi} c_i z^i \right) \left( \sum_{j=0}^{\eta} c'_j z^j \right).
\]

Here $z$ denotes a symbolic variable.

- **Decryption:** For a ciphertext $ct = (c_0, c_1, \ldots, c_\xi)$, the decryption with secret key $sk = s$ is computed by

\[
Dec(ct, sk) = [\tilde{m}]_q \mod t \in R_t,
\]
where \( \bar{m} = \sum_{i=0}^{\xi} c_i s^i \in R_q \) and \([z]_q\) denotes the reduction of \( z \) under modulo \( q \) in the interval \([-q/2, q/2)\).

For the secret key vector \( s = (1, s, s^2, \ldots) \), we can simply rewrite \( \text{Dec}(ct, sk) = [(ct, s)]_q \mod t \).

### 2.2. Homomorphic correctness

The “homomorphic correctness” means that the decryption procedure can correctly recover the operated result over plaintexts after performing several homomorphic operations over ciphertexts. For given ciphertexts \( ct_1 \) and \( ct_2 \) corresponding to the plaintexts \( m_1 \) and \( m_2 \), one has from the construction that

\[
\begin{align*}
\langle ct_1 + ct_2, s \rangle &= \langle ct_1, s \rangle + \langle ct_2, s \rangle, \\
\langle ct_1 \cdot ct_2, s \rangle &= \langle ct_1, s \rangle \cdot \langle ct_2, s \rangle.
\end{align*}
\]

It follows from [5, Theorem 3.3] that homomorphic operations over ciphertexts correspond to the ring structure of the plaintext space \( R_t \). Specially we have

\[
\begin{align*}
\text{Dec}(ct_1 + ct_2, sk) &= m_1 + m_2 \in R_t, \\
\text{Dec}(ct_1 \cdot ct_2, sk) &= m_1 \times m_2 \in R_t.
\end{align*}
\]

**Remark 2.** Given a fresh ciphertext \( ct = (c_0, c_1) \) generated by \( (1) \), we have

\[
\langle ct, s \rangle = (p_0 u + tg + m) + s \cdot (p_1 u + tf) = m + t \cdot (g + sf - ue)
\]

in the ring \( R_q \) since \( p_0 + p_1 s = -te \). If the value \( m + t \cdot (g + sf - ue) \) does not wrap around \( \mod q \) (all errors \( e, f, g, u \leftarrow \chi \) must be sufficiently small), we have \( [(ct, s)]_q = m + t \cdot (g + sf - ue) \) in the base ring \( R \). Hence we can recover the correct plaintext \( m \) by \( \mod t \)-operation, which shows the decryption mechanism for any fresh ciphertexts (see [3] for more details on the homomorphic correctness).

The following Lemma gives us the condition for the homomorphic correctness of the SHE scheme and for choosing suitable parameters \( (n, q, t, \sigma) \) in order to avoid decryption failure of a ciphertext.

**Lemma 3** (Condition for successful decryption). For a ciphertext \( ct \), the decryption \( \text{Dec}(ct, sk) \) recovers the correct result if it satisfies the condition

\[
\|\langle ct, s \rangle\|_\infty < \frac{q}{2}.
\]

Here for \( a = \sum_{i=0}^{n-1} a_i x^i \in R \), let \( \|a\|_\infty = \max|a_i| \) denote the \( \infty \)-norm of its coefficient representation.
3. Previous packing methods

In this section, we briefly review the previous packing methods proposed by Lauter et al. [5] and Yasuda et al. [9], [10] over the SHE scheme described in the previous section.

3.1. Packing method by [5] for large integers

Lauter et al. [5] introduced a method to pack a large integer into a single ciphertext, and it enables efficient computation of sums and products over packed ciphertexts. Specially, a message $M$ of up to $n$ bits is broken into a binary vector $(m_0, \ldots, m_{n-1})$ and associated with a polynomial

$$\text{pm}(M) = \sum_{i=0}^{n-1} m_i x^i,$$

of degree less than or equal to $n - 1$ and finally encrypts $M$ as $\text{ct}(M) := \text{Enc}(\text{pm}(M), \text{pk})$. Note that $\text{pm}(M)|_{x=2} = M$. Moreover, for given plaintexts $M, M'$, the homomorphic addition $\text{ct}(M) + \text{ct}(M')$ gives the polynomial addition $\text{pm}(M) + \text{pm}(M')$ on encrypted data under the correctness [5, Lemma 3.3]. However, the integer multiplication causes a problem as polynomial multiplication $\text{pm}(M) \cdot \text{pm}(M')$ has degree larger than $n$. Hence, their method can only encodes integers of at most $(n/d)$-bit when it requires $d$ homomorphic multiplications. Their method is effective in computing low degree multiplications.

3.2. Packing method by [9] for secure inner product

Yasuda et al. [9] proposed two types of packed ciphertexts as follows: For an integral vector $A = (a_0, \ldots, a_{m-1})$ of length $m \leq n$, set

$$\text{pm}^{(1)}(A) = \sum_{i=0}^{m-1} a_i x^i \quad \text{and} \quad \text{pm}^{(2)}(A) = -\sum_{i=0}^{m-1} a_i x^{n-i},$$

and then packed ciphertexts are defined as $\text{ct}^{(i)}(A) := \text{Enc}(\text{pm}^{(i)}(A), \text{pk})$ for $i = 1, 2$. The first type is same as Lauter et al.’s method [5], but the second one enables efficient secure computation of inner product. Specially, for two vectors $A$ and $B$ of same length $m \leq n$, only one homomorphic multiplication over $\text{ct}^{(1)}(A)$ and $\text{ct}^{(2)}(B)$ can give the inner product $\langle A, B \rangle$ on encrypted data; see [9] Proposition 1]. This computation is effective for secure distance computations such as the Euclidean and the Hamming distances; see [9] for more details.
3.3. Modification by [10] for large integer entries

When handling with small integers (e.g., 1 or 2 bits), the packing method in [9] is effective and sufficient for secure statistics. However, for integers of practical bit-size (e.g., even for 16 or 32 bit), the packing method enforces to set the parameter $t$ of the plaintext space $R_t$ to be considerably large, and it then causes slow performance of the SHE scheme. Yasuda et al. [10] modified the packing method in [9] for large-size integers as follows.

Given an integer $m$, let $A = (a_0, \ldots, a_{m-1})$ be a vector of length $m$ with entries $a_i$ less than $p$ bits. For a chosen integer $r > 0$ (e.g., $r = 2$), we write each integral entry $a_i$ in the base-$r$ representation, namely

$$a_i = \sum_{u=0}^{d-1} a_{i,u} r^u \quad \text{with} \quad a_{i,u} \in \{0, 1, \ldots, r-1\},$$

where $d = \lceil \log r / 2 \rceil$. We associate to $A$ the following two polynomials:

$$\text{pm}_{m,p,r}^{(1)}(A) = \sum_{i=0}^{m-1} \left( \sum_{u=0}^{d-1} a_{i,u} x^u \right) x^{2id},$$

(4)

$$\text{pm}_{m,p,r}^{(2)}(A) = -\sum_{i=0}^{m-1} \left( \sum_{u=0}^{d-1} a_{i,u} x^u \right) x^{n-2id},$$

(5)

and then packed ciphertexts are defined as $ct^{(i)}_{m,p,r}(A) := \text{Enc}(\text{pm}_{m,p,r}^{(i)}(A), pk)$ for $i = 1, 2$. Then we have the following theorem [10, Theorem 1] on secure inner product between $A$ and $B$ with large entries.

**Theorem 4.** Let $A = (a_0, \ldots, a_{m-1})$ and $B = (b_0, \ldots, b_{m-1})$ be two integral vectors of same length $m$ with entries less than $p$ bits. Assume that $n \geq 2md$ and $t > m(r - 1)^2d$, where $d = \lceil \log r / 2 \rceil$ for a fixed positive integer $r$. Let $ct = ct^{(1)}_{m,p,r}(A) \ast ct^{(2)}_{m,p,r}(B)$, and let $\text{Dec}(ct, pk) = \sum_{i=0}^{n-1} m_i x^i \in R_t$ denote the decryption result. Then under the condition of Lemma 1 for the ciphertext $ct$, the sum $\sum_{i=0}^{2d-1} m_i r^i \in \mathbb{Z}$ gives the inner product $\langle A, B \rangle$.

4. Our packing methods for secure matrix multiplication

In this section, we propose packing methods to efficiently compute secure matrix multiplication. Notice that by using the packing methods of Yasuda et al. [9], [10] described in the previous section, one needs $m^2$ secure inner product computations (i.e., $m^2$ homomorphic multiplications) to compute a matrix
multiplication of two $m \times m$ matrices. In this section, for each of the packing method proposed by Yasuda et al., we propose two methods to reduce the number of computations: the first one requires $m$ homomorphic multiplications and the second one requires only one homomorphic multiplication.

4.1. Binary matrix multiplication

Let $A$ be an $m \times m$ matrix with binary entries. Let $A_1, \ldots, A_m$ denote the row vectors of $A$ and $A^T_1, \ldots, A^T_m$ the column vectors of $A$. In order to compute the matrix multiplication $AB$ of two binary matrices $A$ and $B$, one needs to compute the inner products $\langle A_i, B^T_j \rangle$ for $i,j = 1, \ldots, m$. We pack each row $A_i = (a_{i,0}, \ldots, a_{i,m-1})$ and each column $A^T_j = (a_{j,0}, \ldots, a_{j,m-1})$ of $A$ as in (3), namely:

\[
\begin{align*}
\text{pm}^{(1)}(A_i) &= \sum_{u=0}^{m-1} a_{i,u}x^u, \\
\text{pm}^{(2)}(A^T_j) &= -\sum_{v=0}^{m-1} a_{j,v}x^{n-v}.
\end{align*}
\]

It follows from Section 3.2 that the constant term of $\text{pm}^{(1)}(A_i) \times \text{pm}^{(2)}(B^T_j)$ is the inner product $\langle A_i, B^T_j \rangle$ (note that “×” denotes the multiplication in the ring $R = \mathbb{Z}[x]/(x^n + 1)$). We define the following two types of polynomials in $R$ associated to a given matrix $A$:

\[
\begin{align*}
\text{Pol}^{(1)}(A) &= \text{pm}^{(1)}(A_1) + \cdots + \text{pm}^{(1)}(A_m)x^{m(m-1)} \\
&= \sum_{i=1}^{m} \text{pm}^{(1)}(A_i)x^{(i-1)m}, \\
\text{Pol}^{(2)}(A) &= \text{pm}^{(2)}(A^T_1) + \cdots + \text{pm}^{(2)}(A^T_m)x^{m^2(m-1)} \\
&= \sum_{j=1}^{m} \text{pm}^{(2)}(A^T_j)x^{(j-1)m^2}.
\end{align*}
\]

Define the packed ciphertexts for a given matrix $A$ to be

\[
\text{ct}_{\text{mat}}^{(i)}(A) := \text{Enc}(\text{Pol}^{(i)}(A), \text{pk}), \quad \text{for} \quad i = 1, 2.
\]

In order to get the matrix multiplication $AB$, we need to obtain the inner products $\langle A_i, B^T_j \rangle$ for $i,j = 1, \ldots, m$. Here we present our two approaches.

**Theorem 5** (The first packing method). Assume that $n \geq m^2$. For each $j = 1, \ldots, m$, let

\[
\text{ct}_j = \text{ct}_{\text{mat}}^{(1)}(A) * \text{ct}^{(2)}(B^T_j)
\]

and let $\text{Dec}$(ct$_j,$sk) $\in R_t$ denote the decryption result. Then under the condition of Lemma 3 for the ciphertext ct$_j$, for each $j = 1, \ldots, m$, the inner product $\langle A_i, B^T_j \rangle$ is the coefficient of $x^{(i-1)m}$ in $\text{Dec}$(ct$_j,$sk).
Proof. It follows from Section 3.2 (and clear from definition) that
\[ \text{pm}^{(1)}(A_i) \times \text{pm}^{(2)}(B_j^T) = \langle A_i, B_j^T \rangle + \text{other terms of degree } (n - v + u), \]
with \( u \neq v \) and \( u, v \in \{0, \ldots, m - 1\} \), and so the constant term of \( \text{pm}^{(1)}(A_i) \times \text{pm}^{(2)}(B_j^T) \in R \) gives us the inner product of \( A_i \) and \( B_j^T \). Now one has that
\[
\text{Dec}(ct_j, sk) = \text{Pol}^{(1)}(A) \times \text{pm}^{(2)}(B_j^T) = \sum_{i=1}^{m} \text{pm}^{(1)}(A_i) \times \text{pm}^{(2)}(B_j^T)x^{(i-1)m}.
\]
For a fixed index \( i \), we have
\[
\text{pm}^{(1)}(A_i) \times \text{pm}^{(2)}(B_j^T)x^{(i-1)m} = \langle A_i, B_j^T \rangle x^{(i-1)m} + \text{other terms of degree } n - v + u + (i-1)m,
\]
with \( u \neq v \) and \( u, v \in \{0, \ldots, m - 1\} \). Since the exponents of \( x \) is modulo \( n \) and therefore \( n - v + u + (k-1)m \) is never equal to \((i-1)m\) for \( u \neq v \) and \( i, k \in \{1, \ldots, m\} \). This implies that the terms of degree \( (i-1)m \) in \( \text{Pol}^{(1)}(A) \times \text{pm}^{(2)}(B_j^T) \) is exactly the term of degree \( (i-1)m \) in \( \text{pm}^{(1)}(A_i) \times \text{pm}^{(2)}(B_j^T)x^{(i-1)m} \). Hence the inner product \( \langle A_i, B_j^T \rangle \) is the coefficient of \( x^{(i-1)m} \) in \( \text{Dec}(ct_j, sk) \). □

Remark 6. With this approach, after \( m \) homomorphic multiplications over the SHE scheme, we get the resulting matrix multiplication \( AB \).

Theorem 7 (The second packing method). Assume that \( n \geq m^3 \). For each \( j = 1, \ldots, m \), let
\[
ct = ct_{\text{mat}}^{(1)}(A) \ast ct_{\text{mat}}^{(2)}(B)
\]
and let \( \text{Dec}(ct, sk) \in R_t \) denote the decryption result. Then under the condition of Lemma 5 for the ciphertext \( ct \), for each \( i \) and \( j \), the inner product \( \langle A_i, B_j^T \rangle \) is the coefficient of \( x^{(j-1)m^2+(i-1)m} \) in \( \text{Dec}(ct, sk) \).

Proof. The proof is similar to that of Theorem 5. Over the ring \( R \), we have
\[
\text{Dec}(ct, sk) = \text{Pol}^{(1)}(A) \times \text{Pol}^{(2)}(B) = \sum_{i=1}^{m} \sum_{j=1}^{m} \text{pm}^{(1)}(A_i) \times \text{pm}^{(2)}(B_j^T)x^{(j-1)m^2+(i-1)m}.
\]
As in the proof of Theorem 5, the term of degree \( (j-1)m^2+(i-1)m \) in \( \text{Dec}(ct, sk) \) is exactly the term of degree \( (j-1)m^2+(i-1)m \) in \( \text{pm}^{(1)}(A_i) \times \text{pm}^{(2)}(B_j^T)x^{(j-1)m^2+(i-1)m} \in R \). Hence the coefficient of \( x^{(j-1)m^2+(i-1)m} \) in \( \text{Dec}(ct, sk) \) gives the inner product \( \langle A_i, B_j^T \rangle \). □
Remark 8. In this approach, we need to do only one homomorphic multiplication on packed ciphertexts for secure matrix multiplication.

4.2. Non-binary matrix multiplication

In this subsection, we adapt the method in Yasuda et al. [10] described in Section 3.3 to propose packing methods for multiplying integral matrices with non-binary entries. Let $A$ and $B$ be two $m \times m$ matrices whose entries are integers of less than $p$ bits. Let $A^{(1)}, \ldots, A^{(m)}$ and $B^{(1)}, \ldots, B^{(m)}$ be the rows of $A$ and $B^T$, respectively. For $i = 0, \ldots, m - 1$, we write $A^{(i)} = (a_0^{(i)}, \ldots, a_{m-1}^{(i)})$ and $B^{(i)} = (b_0^{(i)}, \ldots, b_{m-1}^{(i)})$. For a chosen integer $r > 0$, we write each integral entry $a_k^{(i)}$ in the base-$r$ representation, namely

$$a_k^{(i)} = \sum_{u=0}^{d-1} a_{ku}^{(i)} r^u \quad \text{with} \quad a_{ku}^{(i)} \in \{0, 1, \ldots, r-1\},$$

where $d = \lceil \log_r 2^p \rceil$, as in Section 3.3 (we took $r = 2$ in our experiments, and then $d = p$). We pack $a_k^{(i)}$ as

$$a_k^{(i)}(x) := \sum_{u=0}^{d-1} a_{ku}^{(i)} x^u \in R = \mathbb{Z}[x]/(x^n + 1).$$

We associate to each row $A^{(i)}$ and column $B^{(j)}$ of $A$ and $B$, respectively, the following polynomials in the ring $R$:

$$\text{pm}_{m,p,r}^{(1)}(A^{(i)}) = \sum_{k=0}^{m-1} a_k^{(i)}(x) x^{2kd},$$

$$\text{pm}_{m,p,r}^{(2)}(B^{(j)}) = -\sum_{l=0}^{m-1} b_l^{(j)}(x) x^{n-2ld},$$

We define the following polynomials in $R$ associated to $A$ and $B$:

$$\text{Pol}^{(1)}(A) := \text{pm}_{m,p,r}^{(1)}(A^{(1)}) + \ldots + \text{pm}_{m,p,r}^{(1)}(A^{(m)}) x^{(m-1)2md}$$

$$= \sum_{i=1}^{m} \text{pm}_{m,p,r}^{(1)}(A^{(i)}) x^{(i-1)2md},$$

$$\text{Pol}^{(2)}(B) := \text{pm}_{m,p,r}^{(2)}(B^{(1)}) + \ldots + \text{pm}_{m,p,r}^{(2)}(B^{(m)}) x^{(m-1)2m^2d}$$

$$= \sum_{j=1}^{m} \text{pm}_{m,p,r}^{(2)}(B^{(j)}) x^{(j-1)2m^2d}.$$
Define the packed ciphertexts for a given matrix \( A \) to be
\[
ct_{\text{mat}}^{(i)}(A) := \text{Enc}(\text{Pol}^{(i)}(A), pk), \quad \text{for } i = 1, 2.
\]
As in the case of binary matrix multiplication, we here present two approaches.

**Theorem 9** (The first packing method). Assume that \( n \geq 2md(m+1) \). For each \( j = 1, \ldots, m \), let
\[
ct_j = ct_{\text{mat}}(A) * ct_{m,p,r}^{(2)}(B^{(j)}),
\]
and let \( \text{Dec}(ct_j, sk) \in R_t \) denote the decryption result. Then under the condition of Lemma \( \text{Lemma 3} \) for the ciphertext \( ct_j \), for each \( j = 1, \ldots, m \), the inner product \( \langle A^{(i)}, B^{(j)} \rangle \) is the sum of the terms of degree greater or equal to \((i-1)2md\) and less than \((i-1)2md + 2d\) in \( \text{Dec}(ct_j, sk) \) evaluated at \( x = r \).

**Proof.** Fix \( i \) and \( j \). Over the ring \( R \), we have that
\[
\begin{align*}
\text{pm}^{(1)}_{m,p,r}(A^{(i)}) \times \text{pm}^{(2)}_{m,p,r}(B^{(j)}) &= \sum_{k=0}^{m-1} a_k^{(i)}(x)b_k^{(j)}(x) \\
&+ \left( -\sum_{k=0}^{m-1} \sum_{l=0}^{k-1} a_k^{(i)}(x)b_l^{(j)}(x)x^{n+(k-l)2d} \right) \\
&+ \left( -\sum_{k=0}^{m-1} \sum_{l=k+1}^{m-1} a_k^{(i)}(x)b_l^{(j)}(x)x^{n+(k-l)2d} \right).
\end{align*}
\]
One gets that the degrees of terms in \( I(i) \), \( II(i) \) and \( III(i) \) are in the intervals \([0, 2d - 2], [2d, 2md - 2] \) and \([n-(m-1)2d, n-2] \), respectively. It follows that the sum of terms of degree less than \( 2d \) in \( \text{pm}^{(1)}_{m,p,r}(A^{(i)}) \times \text{pm}^{(2)}_{m,p,r}(B^{(j)}) \) \( \in R \) evaluated at \( x = r \) gives us the inner product \( \langle A^{(i)}, B^{(j)} \rangle \). Now for each \( j = 1, \ldots, m \)
\[
\text{Dec}(ct_j, sk) = \text{Pol}^{(1)}(A) \times \text{pm}^{(2)}_{m,p,r}(B^{(j)})
= \sum_{i=1}^{m} \text{pm}^{(1)}_{m,p,r}(A^{(i)}) \times \text{pm}^{(2)}_{m,p,r}(B^{(j)})x^{(i-1)2md}.
\]
It then follows that for every \( i = 1, \ldots, m \) the inner product \( \langle A^{(i)}, B^{(j)} \rangle \) is the sum of terms of degree greater than or equal to \((i-1)2md\) and less than \((i-1)2md + 2d\) in \( \text{Dec}(ct_j, sk) \) evaluated at \( x = r \). \( \square \)

**Theorem 10** (The second packing method). Assume that \( n \geq 2m^3d+2md+2d \). Let
\[
ct = ct_{\text{mat}}^{(1)}(A) * ct_{\text{mat}}^{(2)}(B)
\]
and let $\text{Dec}(ct, sk) \in R_t$ denote the decryption result. Then under the condition of Lemma 3 for the ciphertext $ct$, for each $i$ and $j$, the inner product $\langle A^{(i)}, B^{(j)} \rangle$ is the sum of terms of degree greater than or equal to $(i - 1)2md + (j - 1)2m^2d$ and less than $(i - 1)2md + (j - 1)2m^2d + 2d$ in $\text{Dec}(ct, sk)$ evaluated at $x = r$.

Proof. The proof follows easily from that of Theorems 7 and 9.

5. Implementation and comparison

In this section, we report our implementation results. Specifically, we implemented previous methods for inner product and our methods for secure matrix multiplication. In the next subsection, let us describe how to select parameters of the SHE scheme for each method.

5.1. Selection of parameters

Our selection of parameters is followed from [5, Section 3.2.2]. In this subsection, we describe how to select parameters $(n, q, t, \sigma)$ of the SHE scheme. We divide our description into two cases where the entries of matrices $A$ and $B$ are binary and non-binary.

5.1.1. Case of binary matrix multiplication

Here we assume that the entries of two $m \times m$ matrices $A$ and $B$ are binary. As in [5], we fix $\sigma = 8$. For the matrix size $m$, it is sufficient to take the plaintext modulus as $t = m + 1$ since every entry of $AB$ is not greater than $m$. In our experiments, we take $m = 16$ and $32$ for practical use, and hence we set $t = 17$ and $33$, respectively. Let $ct_1$ and $ct_2$ denote the two ciphertexts obtained by packing a part of or whole $A$ and $B$, respectively. For example, in our first packing method, we have

$$cn_1 = ct_{\text{mat}}^{(1)}(A) \quad \text{and} \quad ct_2 = ct_{\text{pack}}^{(2)}(B_j^T) \quad \text{for some} \quad 1 \leq j \leq m.$$ 

In every packing method, we need to multiply $ct := ct_1 \times ct_2$ over ciphertexts for secure matrix multiplication $AB$. In order to avoid decryption failure of the ciphertext $ct$, it requires $\|\langle ct, s \rangle\|_\infty < q/2$ by Lemma 3. Let $U$ denote an upper bound of the $\infty$-norm size $\|\langle ct', s \rangle\|_\infty$ for any fresh ciphertexts $ct' \in (R_q)^2$. We clearly have

$$\|\langle ct, s \rangle\|_\infty = \|\langle ct_1, s \rangle \cdot \langle ct_2, s \rangle\|_\infty \leq nU^2$$

by the well-known fact that $\|a \cdot b\|_\infty \leq n\|a\|_\infty\|b\|_\infty$ for $a, b \in R = \mathbb{Z}[x]/(x^n + 1)$. According to the experimental estimation of [5], we may take $U = 2t^2\sqrt{n}$ in practice. Therefore it suffices to take a prime $q$ satisfying

$$2n(2t^2\sqrt{n})^2 = 8n^2t^2\sigma^4 \leq q.$$ (9)
With respect to the degree parameter \( n \), the previous method requires \( n \geq m \), our first method \( n \geq m^2 \), and our second method \( n \geq m^3 \). In particular, we take \( n = (2^5)^3 = 32768 \) in our second method for \( m = 32 = 2^5 \). In this case, we approximately have \( q \geq 2^{3+30+10+12} = 2^{55} \) by (9) since \( t \approx 2^5 \). Then we always fix one prime \( q \) of 60-bit for the binary case. Note that with such \( q \), we can avoid decryption failure of \( ct \) in every method since our second method requires the largest \( n \) and \( q \). Furthermore, since the ciphertext modulus \( q \) is less than 64-bit, we estimate that the performance over a 64-bit machine would not change when we use smaller \( q \). We remark that for such \( q \), we need \( n \geq 2048 \) for more than 80-bit security (see [5] for more details on the security).

5.1.2. Case of non-binary matrix multiplication

In this case, we assume that the entries of two \( m \times m \) matrices \( A \) and \( B \) are less than \( p \)-bit. In our experiments, we took \( r = 2 \) and hence \( d = p \) in Theorems [4, 9] and [10]. We fix \( \sigma = 8 \) as in the binary case. Let \( ct_1 \) and \( ct_2 \) denote the two ciphertexts obtained by packing a part of or whole \( A \) and \( B \), respectively. For example, in our second packing method, we have

\[
ct_1 = ct^{(1)}_{\text{mat}}(A) \quad \text{and} \quad ct_2 = ct^{(2)}_{\text{mat}}(B).
\]

In every method, each coefficient of the decryption polynomial of \( ct = ct_1 * ct_2 \) is not greater than \( mp \) (see [10], Section 3.2) for details). Hence, in every method, it is sufficient to take \( t = mp+1 \) as the plaintext modulus. For the degree parameter \( n \), the previous method requires \( n \geq 2mp \) by Theorem [4], our first method \( n \geq 2mp(m+1) \) by Theorem [9] and our second method \( n \geq 2p(m^3 + m + 1) \) by Theorem [10]. In our experiments, we take \( m = 16 \) and \( p = 10 \), and hence we fix \( t = mp+1 = 161 < 2^8 \). In this setting, we take

\[
n = 2^{13} = 8192 \geq 2mp(m+1) \quad \text{and} \quad n = 2^{17} = 131072 \geq 2p(m^3 + m + 1)
\]

in our first and second method, respectively. When we set \( n = 2^{17} \), by inequality [9], it requires \( q \geq 8n^2t^2\sigma^4 \approx 2^{3+34+16+12} = 2^{65} \) in order to avoid decryption failure of \( ct \). In our experiments, we took one prime \( q \) of 70-bit in every method for a margin. For such \( q \), it requires \( n \geq 2048 \) for more than 80-bit security as in the binary case, and hence we set \( n = 2048 \geq 2mp \) in the previous method.

5.2. Implementation results

We implemented the SHE scheme with our packing methods for secure matrix multiplication. Our experiments ran on an Intel Core i7-4790 CPU with 3.60 GHz and 8.00 GB RAM, using [PARI] library [8] (version 2.7.5) in C programs.
SECURE MATRIX MULTIPLICATION OVER LWE-BASED HOMOMORPHIC ENCRYPTION

Table 1. Performance of secure matrix multiplication of $m \times m$ matrices with binary entries.

<table>
<thead>
<tr>
<th>(In seconds)</th>
<th>$m$</th>
<th>$(n, \log_2(q), t, \sigma)$</th>
<th>Encryption</th>
<th>Secure Matrix Mul.</th>
<th>Decryption</th>
<th>Total time</th>
</tr>
</thead>
<tbody>
<tr>
<td>Previous method [9]</td>
<td>16</td>
<td>$(2048, 60, 17, 8)$</td>
<td>0.3987</td>
<td>11.5704</td>
<td>3.5352</td>
<td>15.5043</td>
</tr>
<tr>
<td></td>
<td>32</td>
<td>$(2048, 60, 33, 8)$</td>
<td>0.7950</td>
<td>47.8745</td>
<td>14.063</td>
<td>62.7325</td>
</tr>
<tr>
<td>Our first method</td>
<td>16</td>
<td>$(2048, 60, 17, 8)$</td>
<td>0.0150</td>
<td>0.3064</td>
<td>0.2170</td>
<td>0.5384</td>
</tr>
<tr>
<td></td>
<td>32</td>
<td>$(2048, 60, 33, 8)$</td>
<td>0.0160</td>
<td>0.5780</td>
<td>0.3592</td>
<td>0.9532</td>
</tr>
<tr>
<td>Our second method</td>
<td>16</td>
<td>$(4096, 60, 17, 8)$</td>
<td>0.0439</td>
<td>0.4348</td>
<td>0.0328</td>
<td>0.5115</td>
</tr>
<tr>
<td></td>
<td>32</td>
<td>$(32768, 60, 33, 8)$</td>
<td>0.1814</td>
<td>0.5220</td>
<td>0.4036</td>
<td>1.1070</td>
</tr>
</tbody>
</table>

Table 2. Performance of secure matrix multiplication of $16 \times 16$ matrices with entries less than $p = 10$ bits.

<table>
<thead>
<tr>
<th>(In seconds)</th>
<th>$(n, \log_2(q), t, \sigma)$</th>
<th>Encryption</th>
<th>Secure Matrix Mul.</th>
<th>Decryption</th>
<th>Total time</th>
</tr>
</thead>
<tbody>
<tr>
<td>Previous method [10]</td>
<td>$(2048, 70, 161, 8)$</td>
<td>1.4454</td>
<td>9.4483</td>
<td>3.6581</td>
<td>14.5518</td>
</tr>
<tr>
<td>Our first method</td>
<td>$(8192, 70, 161, 8)$</td>
<td>1.5996</td>
<td>1.5797</td>
<td>0.7624</td>
<td>3.9417</td>
</tr>
<tr>
<td>Our second method</td>
<td>$(131072, 70, 161, 8)$</td>
<td>4.2024</td>
<td>2.0834</td>
<td>0.9797</td>
<td>7.2655</td>
</tr>
</tbody>
</table>

5.2.1. Binary case

Our chosen parameters and implementation results for the binary case are given in Table 1. As shown in Table 1, our two methods are much faster than the previous method [9]. Specifically, our first method is about $2m$ times faster than the previous method [9]. For $m = 16$, our second method is several times faster than the first one. However, for $m = 32$, our first method is slightly faster than the second one in total. This is due to the fact that our second method requires $n \geq m^3 = 32^3 = 32768$ while our first method requires only $n \geq m^2 = 32^2 = 1024$ (as in [9], we took $n \geq 2048$ for enough security). Then we conclude that our second method is still efficient for $m \leq 32$, but our first method becomes much more efficient for $m \geq 64$ in the binary case.

Remark 11. In our experiments, we took $m$ of the special form $m = 2^k$ to make it easier to choose parameters of the SHE scheme (recall that the degree parameter $n$ should be chosen as a 2-power integer). However, performance of secure matrix multiplication does not depend on the form of $m$. Furthermore, for $m$ between $m = 32$ and $64$, we estimate from Table 1 that our first method would be slightly faster than our second one.
5.2.2. Non-binary case

In Table 2, we give a performance comparison of secure matrix multiplication of 16 × 16 matrices (i.e., \( m = 16 \)) with entries less than \( p = 10 \) bits. Our chosen parameters for the SHE scheme and implementation results are given in Table 2. As we can see from the Table 2, our first method is around 5-times faster and second method is around 2-times faster than the previous method. In this case we are not using same parameter for our both the methods as in the previous method. Our first and second methods require \( n = 8192 \geq 2mp(m+1) \) and \( n = 131078 \geq mp(m^3+m+1) \), respectively. Because of very large \( n \) in our second method, our first method is faster than the second method. However, in sense of computation efficiency, our second method is better than the first method because we need \( m \)-homomorphic multiplications for our first method and just only one homomorphic multiplication for our second method. Once we increase the size of entries and dimension of matrices our first method will be much faster.

Remark 12. When we fix \( m \) and take larger \( p \), our first method becomes much faster than our second one since our first method permits us to take smaller parameters of the SHE scheme. Therefore, unfortunately, our second method has advantage on performance only in case of very small \( m \) and \( p \) such as \((m, p) = (16, 5)\). More specifically, for \( m \geq 32 \), our first method is faster than our second one for any \( p \) since our first method is faster even in the binary case from Table 1.

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