In operando mechanism analysis on nanocrystalline silicon anode material for reversible and ultrafast sodium storage

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Abstract
Presently, lithium-ion batteries (LIBs) are the most promising commercialized electrochemical energy storage systems. Unfortunately, the limited resource of Li results in increasing cost for its scalable application and a general consciousness of the need to find new type of energy storage technologies. Very recently, substantial effort has been invested to sodium-ion batteries (SIBs) due to their effectively unlimited nature of sodium resources. Furthermore, the potential of Li/Li+ is 0.3 V lower than that of Na/Na+, which makes it more effective to limit the electrolyte degradation on the outer surface of the electrode.[1] Nevertheless, one major obstacle for the commercial application of SIBs is the larger ionic radius of Na+ (0.98 Å) which is 0.29 Å larger than that of Li+, resulting in easier structural degradation for the Na+ host materials. [2,3] As anode materials for SIBs, the traditional carbon-based materials like hard carbon[4] and porous carbon,[5,6] tin (Sn),[7] and antimony (Sb)[8] show poor cycle performance due to their large volume expansion caused by Na+ insertion.

Keywords
nanocrystalline, storage, silicon, anode, material, sodium, mechanism, reversible, ultrafast, analysis, operando

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In-operando mechanism analysis on nanocrystalline silicon anode material for reversible and ultra-fast sodium storage

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Keywords: nanocrystal silicon, in-operando analysis, anode, sodium ion battery
Presently, lithium-ion batteries (LIBs) are the most promising commercialized electrochemical energy storage systems. Unfortunately, the limited resource of Li results in increasing cost for its scalable application and a general consciousness of the need to find new type of energy storage technologies. Very recently, substantial effort has been invested to sodium-ion batteries (SIBs) due to their effectively unlimited nature of sodium resources. Furthermore, the potential of Li/Li$^+$ is 0.3 V lower than that of Na/Na$^+$, which makes it more effective to limit the electrolyte degradation on the outer surface of the electrode.$^{[1]}$

Nevertheless, one major obstacle for the commercial application of SIBs is the larger ionic radius of Na$^+$ (0.98 Å) which is 0.29 Å larger than that of Li$^+$, resulting in easier structural degradation for the Na$^+$ host materials.$^{[2,3]}$ As anode materials for SIBs, the traditional carbon-based materials like hard carbon$^{[4]}$ and porous carbon$^{[5,6]}$, tin (Sn)$^{[7]}$ and antimony (Sb)$^{[8]}$ show poor cycle performance due to their large volume expansion caused by Na$^+$ insertion.

Compared with the commercial carbon-based anodes with a relatively low theoretical capacity (below 380 mAh g$^{-1}$) in LIBs, silicon (Si)-based materials are particularly promising because of their extremely high reversible specific capacity (3590 mAh g$^{-1}$, corresponding to Li$_{15}$Si$_4$)$^{[9,13]}$. Very excitingly, many previous theoretical calculations have predicted that Na can also alloy with Si to form NaSi (Si + Na$^+$ + e$^-$ $\leftrightarrow$ NaSi), providing a specific capacity around 960 mAh g$^{-1}$.$^{[14,15]}$ Therefore, from the viewpoint of high energy density, Si-based anode material is also an excellent choice for SIBs. Unfortunately, realizing reversible Na$^+$ insertion in crystalline Si (c-Si) is still impeded by difficulties. Only one report on the charge/discharge-reversible Si anode for SIBs was published very recently, where nanosized Si contained a significant amount (more than 60%) of amorphous Si (a-Si) as anodes in SIBs.$^{[1]}$ Owing to the essential disadvantages of Si (e.g., the low conductivity and large volume change during cycling), however, poor rate performance and cycle life were unavoidable. Also, the sodiation mechanism has not been well elaborated so far.
Herein, we demonstrate for the first time the feasibility of nanosized c-Si (below 100 nm in diameter) as the original anode in SIBs. Taking advantages of in-operando Raman spectroscopy that is also first reported for Si-based anodes, and kinetics analysis, we find that an irreversible crystal structure conversion occurs during the first Na\(^+\) insertion/extraction processes, resulting in an irreversible decrease in the crystallinity within the initial c-Si. Ultra-fast reversible reactions between the newly generated a-Si and Na\(^+\) ions then proceeded smoothly during the subsequent cycles. In addition, the size effect of the starting c-Si nanocrystals also has a significant influence on the reversible Na\(^+\) insertion/extraction into/from c-Si. Microsized c-Si showed a very low specific capacity and didn't show any structural conversion in terms of the electrochemical and in-operando X-ray diffraction (XRD) analyses, respectively. Unfortunately, the cyclability of the pure nanosized c-Si was poor, resulting in a rapid decay in capacity even during initial several cycles. This can be explained by the volume expansion/constriction during Na\(^+\) insertion/extraction processes.\[16\]

As a result, structural degradation in the expanded c-Si quickly took place, and these destructive Si particles were subsequently dispersed into the electrolyte, leading to the poor structural stability. Another disadvantage of pure c-Si is the low conductivity, which is negative for rate performance.\[10,11\]

Based on our research on the sodiation mechanism of nanosized c-Si, herein, the electrochemical abilities of pure nanosized c-Si electrode could be improved via carbon doping and nanostructural design. Inspired by the previous work on Si-based anodes in LIBs,\[17-26\] loading Si nanoparticles (Si NPs) within one-dimensional (1D) carbon nanofibers is one of the most desirable strategies to improve the conductivity and rate capability. Such an attractive structure can provide shortened path for ionic diffusion and effective electron transfer in the radial direction. As for the volume expansion of c-Si, yolk-shell carbon/void/Si (CVS) composite is prospective, since the introduced gap provides space for Si NPs without wrecking the outer coatings and the solid electrolyte interphase (SEI) film.\[11,27\] Therefore, by
integrating 1D carbon nanofibers and the yolk-shell structure, it is feasible to create high-performance Si anodes.

In this work, we fabricated a flexible binder-free bamboo-rattle type Si/C film via a electrospinning technology. Typically, CVS nanobeads were first prepared through a templating method, whereby Si NPs were enwrapped with a uniform silica coating (SiO$_2$@Si), and then these SiO$_2$@Si nanospheres were uniformly distributed into the carbon nanofibers through a facile and scalable electrospinning method. After the treatment of carbonization and HF etching, the final Si/C composite with a well-defined yolk-shell nanoarchitecture was achieved. Very encouragingly, the resulting Si/C hybrid is flexible, and exhibits high reversible capacity, ultrafast sodium storage and outstanding cyclability in SIBs.

**Figure S1a** shows the experimental and simulated XRD patterns of nanosized c-Si with Rietveld refinement. All the peaks are strongly correlated to c-Si, and no other peaks can be found.$^{[12]}$ The refinement with a space group of cubic *Fd-3m* (227) gives the lattice parameters of $a = b = c = 5.4271(1)$ Å and $V = 159.85(0)$ Å$^3$ with reasonably low $R$-factors (6.35%) and $x^2$ (0.9358) value using the GSAS suite.$^{[29]}$ Figure S1b shows the schematics of the cubic structure of nanosized c-Si. Figure S1c presents the scanning electron microscopy (SEM) image of the slightly agglomerated nanosized c-Si NPs with a diameter of 80–100 nm. Figure S1d reveals the SEM picture of microsized c-Si with an irregular morphology in the size range of about 2–5 μm. Figure S1e illustrates the high-resolution transmission electron microscopy (HRTEM) image and its corresponding fast Fourier transformation (FFT) pattern of an individual nanosized c-Si particle, which shows the resolved Si lattice planes with a plane distance of about 0.16 nm, consistent with the Si crystal’s (400) planes.$^{[13]}$

**Figure 1a** shows the 1st, 2nd, 50th, 100th and 200th charge and discharge curves of pure nanosized c-Si at a current density of 50 mA g$^{-1}$. The discharge and charge capacities in the first cycle are 869.6 and 170.8 mAh g$^{-1}$ (corresponding to Na$_{0.91}$Si), and there is considerable capacity decrease in the next cycles. Although the pure nanosized c-Si exhibits poor cycling
stability, it is obvious that reversible Na\(^+\) insertion/extraction reactions happen in the pure nanosized c-Si electrode based on these discharge–charge profiles and the inset cyclic voltammetry (CV) data. During the first discharge, a sloping voltage plateau at 0.75 V can be found. However, the plateau becomes less and less obvious in the following cycles. This can be further confirmed by the reduction peaks at about 0.75 V (which follow the same changes in intensity as the platforms at 0.75 V in Figure 1a) in the CV profiles (inset of Figure 1a), indicating the continual reformation of the SEI layers as a result of the repeated pulverization of nanosized c-Si.\(^{[16]}\) Another voltage plateau below 0.5 V in the discharge profile is consistent with the reduction peak at 0.2 V in the CV plot, which is related to the Na ion uptake and alloying process with c-Si.\(^{[1]}\) In addition, the intensity of these reductive peaks around 0.2 V decrease gradually upon cycling, indicating the weaker and weaker active reactions between active c-Si and Na ions in the electrode. This can be explained by the reduced amount of active c-Si, arising from continuous pulverization and volume expansion of nanosized c-Si during discharge/charge cycles.\(^{[16]}\) Compared with the initial discharge profile with obvious long plateaus around 0.5 V, however, no flat plateaus can be found in the following discharge cycles. Therefore, different reaction mechanisms may occur during the Na\(^+\) insertion in the initial and subsequent discharge processes, respectively. The galvanostatic discharge/charge profiles and the CV curves suggest that the initial Na\(^+\) insertion process is more like the typical alloying reaction behavior, while the following ones tend to show the capacitive behavior (Figure S2).\(^{[28]}\) Moreover, the platforms below 0.75 V in the charge profiles correspond to the broad oxidative peaks between 0.01–0.75 V in the CV curves (inset of Figure 1a). Evidently, these curves could not be well overlaped, indicating the poor structural and cycling stability of nanosized c-Si for sodium storage.

Based on CV results, kinetic analyses have been carried out to further understand the electrochemistry of the nanosized c-Si/Na cell. Figure S2a shows the CV profiles with broad
peaks at different scan rates from 0.1–20 mV s\(^{-1}\) during both charge and discharge processes. Based on the relationship between current \(i\) and the scan rate \(v\)[29]:

\[
i = av^b
\]

the \(b\)-value is obtained from the slope of the \(\log(v)–\log(i)\) plots. Particularly, the \(b\)-value of 0.5 demonstrates an 100% diffusion-controlled process, while 1.0 indicates a capacitive behavior. Figure S2b shows the \(\log(v)–\log(i)\) result for the nanosized c-Si electrode. The \(b\)-value is 0.79 for the oxidation peaks at scan rates between 0.2–20 mV s\(^{-1}\), indicative of the kinetics of capacitance-dominated performance. The total capacitive contribution can be quantified by distinguishing the different contribution from the diffusion or capacitance-controlled charge at a certain voltage under a fixed scan rate. Figure S2c shows that the diffusion-controlled charge mostly took place around the peak voltage, suggesting that the diffusion-controlled process is available in this area and consistent with an alloying interaction behavior between Si and Na ions.[30] Depend on the quantification, 68 % of the total charge is capacitive at a scan rate of 10 mV s\(^{-1}\). Contribution ratios between these two different processes at other different scan rates were also tested. The capacitive capacity grows progressively with rising the scan rate, and reaches a maximum value of 68 % at 10 mV s\(^{-1}\) finally (Figure S2d).

The Na\(^+\) storage capability of the microsized c-Si anode was also explored by in-operando XRD patterns (Figure 1b). There is no change in the intensity of the Si peak around 29.7°. This suggests that no structural conversions or reversible reactions could proceed in the microsized c-Si electrode, and the capacity of the microsized c-Si electrode is negligible (see Figure S3). Therefore, the electrochemical behavior of the c-Si strongly depends on the particle size. In another word, the surface area of the c-Si plays a very important role in its final electrochemical performance. The BET surface area of microsized c-Si and nanosized c-Si is around 12 and 36 m\(^2\) g\(^{-1}\), respectively. The surface area of nanosized c-Si is three times
higher than that of the microsized c-Si. Therefore, compared with the microsized c-Si, reversible reactions with Na\(^+\) ions are more available and accessible in the nanosized c-Si. For better insight into the crystal structure change and the electrochemical activity of the nanosized c-Si during sodiation/extraction processes, \textit{in-operando} Raman analyses during the electrochemical reactions were conducted. Figure 2a shows the \textit{in-operando} Raman spectra for the pure nanosized c-Si electrode during the first CV cycle. The acquisition time for each Raman spectrum is 500 s, and each Raman spectrum is consequently acquired within 0.2 V. The potential output of the original half SIB is 1.8 V, and the total measurement time for the first CV cycle is 12,000 s. The peak located at about 520 cm\(^{-1}\) is attributed to c-Si, while the peaks at about 1350 cm\(^{-1}\) (D-band) and 1600 cm\(^{-1}\) (G-band) are ascribed to carbon.\(^{[11]}\) No other sharp peaks could be found from the charged/discharged electrode. The intensity of the Si peak in the Raman spectrum is directly related to its crystal structure, and a larger peak area corresponds to relatively higher crystallinity and vice versa. Therefore, the structural changes (e.g., structural disorder) due to charge/discharge energy storage will inevitably lead experimentally to the evolution of band intensities. Therefore, revealing the crystal structural information on c-Si through \textit{in-operando} Raman testing is ideally suitable to obtain a clear relationship between the structural features and Na\(^+\) insertion/extraction. The intensity changes in the Si peaks are summarized in Figure 2b. It is revealed that the relative intensity of the Si peaks first decreased significantly after the initial discharge process (0–4500 s), corresponding to the reduction peaks at 0.75 V (from 0.5 V to 1.5 V) and 0.2 V (from 0.01 V to 0.5 V) in the CV profile. Then the decrease slowed down in the following charge process (4,500–12,000 s), and the peak intensities remained stable for the rest of the cycling test (12,000–30,000 s). Because the reduction peak at 0.5–1.5 V disappeared during the second cycle, the first intensity decrease in the range of 0–3250 s should be related to the formation of SEI film during the first discharge process. The intensity then decreased from 3250–4500 s. This corresponds to another reduction peak from 0.01 V to 0.5 V, demonstrating the Na\(^+\)
uptake and the alloying process with c-Si. After the first discharge process, the intensity of the Si peak remained almost stable, which means that the intensity decrease from Na⁺ insertion was irreversible. Therefore, we believe that the crystallinity of c-Si experienced a considerable irreversible decrease during the first cycling test, resulting in more of the disordered a-Si structure in the c-Si (which can be testified by the HRTEM images and their corresponding FFT patterns in Figure S4). HRTEM images indicate the crystalline nature of original c-Si nanoparticles (Figure S4a) and an increasing content of the amorphous structure in the subsequent cycle (Figure S4b and S4c), which agrees well with the Raman results. Furthermore, the crystal structural conversion of the c-Si electrode was explored in detail by ex-operando Raman spectra. Figure 2c shows the Raman profiles of the original c-Si and the c-Si after one electrochemical cycle. It can be found that the intensity of the Si peak decreased directly from 6800 a.u. to 1000 a.u. after the first cycle. In addition, a certain amount of a-Si could be observed after one cycle (inset of Figure 2c). The ratio of the peak areas between a-Si and c-Si is about 3:1, which means that more than 75% of the c-Si was converted to a-Si after the first sodiation process.

Therefore, an irreversible crystal structure transformation from c-Si to a-Si takes place during the first sodiation process and this newly generated a-Si is beneficial for the reversible Na⁺ insertion reaction due to its more disordered crystal structure. Unfortunately, because a-Si has a larger lattice spacing than that of c-Si, the volume of the original c-Si will inevitably increase during this sodiation process. The structural integrity will also be destroyed, leading to a degradation of the original structure and the decrease of the effective conductive contact between the electrode skeleton and the active materials (similar to the c-Si’-behavior in LIBs). As a result, the pure nanosized c-Si electrode shows poor structural and cycling stability.

The diagrams in Figure S5 schematically illustrate the process for the c-Si planes collapse. The whole process includes four phases along with an growing degree of sodiation. In stage one, Na ions diffuse along the ion channels and accumulate at tetrahedral sites between (111)
planes because this position is the most stable site for Li- or Na-ion insertion (highlighted as greenish regions in Figure S5a). In stage two, along with the concentration of Na\(^+\) grows, the bonds of Si-Si are broken, and Na atoms make new bonds with Si atoms in stable positions (Figure S5b). In stage three, further sodiation leads to the break of majority of the Si-Si bonds, and crystal Si transformed into an amorphous Na-Si alloy (Figure S5c). In stage four, after the desodiation process, the amorphous structure is maintained, leading to the a-Si structure (Figure S5d).

To enhance the architectural stability and the conductivity of c-Si (Figure S6), we design a unique nanoarchitectured Si/C composite. It is demonstrated that only limited sodiation can proceed in the microsized c-Si, due to the limited active positions. In contrast, the sodiation process could be more easily realized in the nanosized c-Si. However, the nanosized c-Si experiences large volume expansion after the first sodiation, resulting in structural pulverization. Therefore, a yolk-shell structure inside the 1-D structure of the Si/C composite is reasonable for improving the structural stability and also the conductivity. Yolk-shell structured Si/C nanobeads distributed within the 1-D carbon nanofibers lead to a bamboo-rattle like Si/C composite. Even after the sodiation process, the cracked c-Si particles can still be well maintained inside the hollow carbon spheres, avoiding the direct exposure of the electrodes in the electrolyte.

Figure 3 illustrates the synthesis procedure for the Si/C composite. c-Si NPs are first coated with silica to obtain the SiO\(_2\)/Si composite, and then the SiO\(_2\)/Si composite is mixed with polyacrylonitrile (PAN) via an electrospinning process to prepare the SiO\(_2\)/Si/PAN nanofibers. After the carbonization and HF treatment, the flexible Si/C film composed of Si/C nanofibers is obtained. A single Si/C nanofiber is also shown in the center of Figure 3 along with its enlarged cross section from one end. Yolk-shell structured CVS is embedded inside the bamboo-like Si/C nanofiber, leading to a bamboo-rattle structured Si/C composite. Figure S7 shows the SEM image for the SiO\(_2@\)Si particles. The coating layers of silica were
Successfully created on each Si NP. Comparing with the pure nanosized c-Si in 1c, the diameters of Si NPs obviously increase from around 80–120 nm to 100–160 nm due to the silica coating on the outer surface. Therefore, the thickness of the silica sacrificial layer is in the range of 10–20 nm. Figure 4 reveals the SEM images of the final Si/C composites. The nanofibers from all the samples are well interconnected, leading to a connected three-dimensional (3D) network. The average diameter of the nanofibers was around 300–500 nm. A series of bead-like expansions can be found along the bamboo-structured nanofibers. In addition, with increasing the c-Si content, the surface of the carbon nanofibers becomes much rougher, and the size of the interior bead-like expansions is also increased. This phenomenon becomes quite obvious when the SiO$_2$@Si content is increased to 0.7 g, which is mainly due to the poor dispersion of SiO$_2$@Si particles in the solvent for the electrospinning process. The typical TEM results of the final Si/C composites are also shown in Figure 4. With increasing the content of the active materials, more CVS nanobeads can be found inside the nanofibers, leading to much more irregular morphologies. Although the contents of SiO$_2$@Si (0.4 g, 0.5 g to 0.6 g) are different, the sizes of the CVS are nearly the same. The diameter of a whole CVS nanofiber is typically around 150–160 nm. The SEM pictures of c-Si and SiO$_2$@Si display that all the nanoparticles have slight agglomeration. Nevertheless, the TEM images clearly show that Si NPs are separate from each other with a void space between them. It is believed that the uniform coating of silica and the following treatment by HF can improve the CVS distribution inside the nanofibers. TEM observations indicate that the silica coatings could be completely removed after HF treatment, suggesting that the HF solution could go through the CNF wall. As a result, it is reasonable that few amount of the electrolyte could also penetrate the CNF wall during the cycling of our tested cells. Owing to the formation of SEI layers on the outer surface of CNFs during initial cycles, however, it is believed that the electrolyte could be effectively limited to go through the CNF wall in the subsequent cycles.
The mechanical properties (mechanical intensity and flexibility) of the final products play important roles in the practical application of flexible electrodes. Since an atomic force microscopy (AFM) system can be satisfied to accurately employ nano- and pico-Newton ranged forces to detect the nanoscale ranged deformation, it is an extremely helpful instrument for the investigating the mechanical properties of nanosized materials. The working principle of this tool is depended on the interaction between the sample and AFM tip upon contact or near contact, in the so-called three-point bend measurement. Therefore, it is ideally available for revealing the flexibility of the nanosized Si/C fibers. Figure S8a shows a model of this three-point bend test system. One single Si/C nanofiber with the AFM tip pressed upon it is suspended over a void between two nanofibers. Figure S8b shows the relationship between the deformation distance and the voltage. The pressure force imposed on the nanofiber has a relationship with the voltage. The higher the voltage, the bigger was the pressure on the sample. Therefore, based on the ratio between the distance (\(D\)) and the voltage (\(V\)) \((dD/dV)\), the flexibility differences between the different samples could be obtained. The \(dD/dV\) of the straight line section in the profiles of Si/C-1, Si/C-2, Si/C-3, and Si/C-4 is around 283, 270, 250, and 180 nm V\(^{-1}\), respectively. In order to guarantee the reliability and repetitability of the results for each sample, nine different positions in each sample were also investigated. It is found that all these data were distributed in a narrow range. Si/C-1 shows the best flexibility, while Si/C-4 has the worst. However, no big difference in the flexibility can be found among the Si/C-1, Si/C-2 and Si/C-3 samples. If we take the contents of the active materials (c-Si) in the Si/C composites into consideration, the Si/C-3 is endowed with a high active material content along with good flexibility. Therefore, Si/C-3 would have a higher specific capacity and also the possibility for the binder-free flexible cell fabrication. Si/C-3 membrane recovers its initial state easily after folding and manipulation, indicating its excellent mechanical durability (Figure S8c). Si/C-3 as the preferred sample was chosen for the following electrochemical tests.
Figure S8d illustrates the XRD results of Si/C-3 and CNFs. Three distinct diffraction peaks at 2θ values of 28.3°, 47.0°, and 55.8° can be ascribed to the Si phase’s (111), (220) and (311) plans, respectively. Apart from them, another peak at around 25.0° can be associated with the carbon’s (002) planes. The intensity of (002) peak is very weak, which means that this carbon material has a low graphitization degree. Figure S8e shows the Thermogravimetric (TG) curve of the Si/C-3 sample in air. The weight loss caused by carbon combustion is started from 450 °C and ended at about 670 °C. The residual mass of Si/C-3 at 670 °C is around 20 wt%, suggesting the relevant amount of c-Si. In addition, a slight weight increase can be found after 670 °C, which is mainly assigned to the oxidation of c-Si at high temperature in air.

Figure 5a shows the initial/second discharge and charge profiles of Si/C-3 at a current density of 50 mA g⁻¹. The discharge and charge capacities in the first cycle are 813.7 and 438.0 mAh g⁻¹, leading to an initial Coulombic efficiency of 53.8%. During the first discharge, the sloping voltage plateaus which disappear in the second cycle between 1.0 and 0.4 V illustrate the formation of the stable SEI film on the outer surface of the electrode, corresponding to the reduction CV peak at around 0.5 V in Figure 5b. A flat voltage plateau below 0.4 V is consistent with the reduction peak at about 0.2 V, indicating the insertion of sodium ions in c-Si. Furthermore, the voltage plateaus around 0.5 V during the charge processes in the charge-discharge profiles and also the corresponding oxidation peaks below 0.5 V in the CV curves are related to the extraction of Na⁺ from Si. Compared with the pure c-Si electrode, a new pair of redox peaks in the range of 0.5–2.0 V can be found in the CV profiles of Si/C, indicating the extraction of Na⁺ from CNFs.

The cycling performance in Figure 5c reveals that the charge capacity of Si/C-3 from 0.01 to 3 V at 50 mA g⁻¹ is 438.2 mAh g⁻¹ at the first cycle and 454.5 mAh g⁻¹ after 200 cycles, indicating that Si/C-3 has an excellent cyclability. In addition, the charge capacity of pure carbon nanofibers derived from pure PAN is 154.7 mAh g⁻¹ at the first cycle and 156.9 mAh
g\(^{-1}\) after 200 cycles. Compared with CNFs, the higher capacity 290 mAh g\(^{-1}\) of Si/C-3 is derived from the introduction of c-Si. Figure 5d shows the rate performance of Si/C-3 at different current densities. The charge capacity of Si/C-3 at 50 mA g\(^{-1}\) after the first 20 cycles is 408.5 mAh g\(^{-1}\), and the capacity retention at 100, 500, 1000, 2000 and 5000 mA g\(^{-1}\) after 20 cycles is 94.5, 89.1, 81.6, 75.4 and 65.9\%, respectively. Furthermore, the long-term cycling performance of Si/C-3 at a higher current density of 5000 mA g\(^{-1}\) is shown in Figure 5e. TG results indicate that there are 20 wt% of silicon inside the Si/C-3 sample. In addition, based on the theoretical capacity of Si (about 960 mAh g\(^{-1}\)) and hard carbon (around 300 mAh g\(^{-1}\)) as anoded in SIBs, the 1 C rate for Si/C-3 should be 432 mA g\(^{-1}\). The discharge capacity of Si/C-3 is maintained at about 75\% even after 2000 cycles at a very high current density (over 10 C, 1C = 432 mA g\(^{-1}\)). Therefore, the as-prepared Si/C-3 is endowed with the excellent cyclability and rate performance. In order to systematically understand the whole profiles about the alloy anodes for sodium storage, we provide a summary in Table S1 to compare our Si-based alloy electrode with other alloy materials. The TEM image of Si/C-3 upon continuous cycling is shown in Figure S9. We can see that a structural pulverization of c-Si happens inside Si/C-3 after cycling test. c-Si nanoparticles are broken into smaller particles in an amorphous state. Fortunately, all of these smaller-sized particles are well maintained inside the porous CNFs, which is exactly ascribed to our novel structural design.

The bamboo-rattle structured Si/C created here shows excellent electrochemical properties with good cycling stability and ultra-fast Na\(^+\)-storage capability. This is mainly because of its unique architecture: (1) the introduction of the interconnected 1D carbon nanofiber network can effectively strengthen the mechanical properties, improve the electrical conductivity, and prevent electrolyte ingress; and (2) the yolk-shell structure developed inside can not only provide additional storage sites for Na\(^+\) and promote Na\(^+\) transfer and electrolyte diffusion, but also provide better absorption of the huge volume changes of c-Si during cycling, prevent the aggregation of Si NPs, and allow for the growth of a stable SEI film.
In summary, we report the sodiation/desodiation mechanism of nanocrystalline silicon during the Na$^+$ insertion/extraction processes. It is found that an irreversible crystal structure conversion from crystalline silicon to amorphous silicon takes place during the first discharge process, leading to reversible Na$^+$ insertion in the newly generated amorphous silicon. Furthermore, in order to overcome the poor conductivity and inferior structural stability of a-Si, an attractive well-defined bamboo-rattle-like architecture is designed. The rationally designed, interconnected, 3D carbon framework, which is made of yolk-shell carbon/silicon nanobeads embedded in 1D carbon nanofibers, endows this silicon-based SIB electrode with excellent electrochemical Na-storage performances. In addition, this 3D carbon framework is flexible and also has good mechanical strength, which can serve directly as binder-free anodes for SIBs. Therefore, we believe that nanocrystalline silicon is a very promising anode material in sodium ion batteries, taking advantages of a designed structure.

**Experimental Section**

*Materials:* All the reagents used were obtained from Sigma Aldrich. Commercial c-Si NPs were first coated with a SiO$_2$ layer (SiO$_2$@Si) using tetraethoxysilane. SiNPs (150 mg) were first dispersed in a mixture of ethanol (240 mL) and water (60 mL) under ultrasonication, followed by addition of concentrated ammonium hydroxide (3.0 mL). Under vigorous stirring, 2.4 g of tetraethoxysilane (TEOS, Aldrich) was added dropwise into the dispersion and the reaction was kept at room temperature under stirring for 12 h. The SiO$_2$-coated SiNPs were collected by centrifugation, and washed for three times by water. Si@SiO$_2$/polyacrylonitrile (SiO$_2$@Si/PAN) nanofibers were first electrospun on a piece of aluminium foil from a precursor solution that contained SiO$_2$@Si (0.4 g), PAN (average $M_w = 150,000, 1.0$ g), and N, N-dimethylformamide (DMF, 10 mL). The voltage of 17 kV was applied over a collector distance of 10 cm and the flow rate was fixed at 1 mL h$^{-1}$. A film of SiO$_2$@Si/PAN nanofiber web was then peeled off from the collector after electrospinning. The flexible binder-free film
made of bamboo-rattle type Si/C nanofibers was finally obtained after calcining at 600 °C for 2 h in 5% Ar/H₂ and then etching the SiO₂ in 10 wt % HF aqueous solution at room temperature for 30 min. The as-obtained sample was denoted as Si/C-1.

For comparison, different Si/C composites with different Si contents were fabricated under the same conditions as described above except that 0.5, 0.6 or 0.7 g SiO₂@Si was added and the samples were denoted as Si/C-2, Si/C-3 and Si/C-4, respectively. In addition, pure PAN without SiO₂@Si was also prepared. After the electrospinning of the PAN solution, it was subjected to a calcination process at 600 °C for 2 h in Ar/H₂ (95/5 by volume) atmosphere to obtain the final pure carbon, identified as carbon nanofibers (CNFs).

**Materials Characterization:** The morphology and microstructure of the products were characterized by scanning electron microscopy (SEM, FEI, Sirion 200). Transmission electron microscopy (TEM) and high-resolution TEM (HRTEM) images were obtained with a Tecnai G2 F30 (FEI, Holland). Thermogravimetric (TG) analysis was carried out with a PerkinElmer Diamond TG/DTA apparatus. X-ray diffraction (XRD) studies were carried out using a X'Pert PRO (PANalytical B.V., Epsilon 5, Holland) diffractometer with high intensity Cu Kα1 irradiation (λ = 1.5406 Å). The in-operando XRD measurements were conducted by combining an electrochemical workstation (CHI 760D) and two-dimensional (2D) XRD (Bruker D8 DISCOVER). Raman spectroscopy was conducted on a LabRAM HR Evolution system employing a 10 mW helium/neon laser at 514.5 nm. In-operando Raman testing was conducted on a LabRAM HR Evolution system equipped with deep-depleted thermoelectrically cooled charge-coupled device (CCD) array detector and an Ar laser (wavelength = 514.5 nm). Raman spectra of the samples were captured one by one via a mapping mode (one spectrum per 250 seconds) while cyclic voltammetry was conducted in an in-operando Raman cell at a sweep rate of 0.4 mV s⁻¹. Atomic force microscopy (AFM) in a tapping mode was employed for the three-point bending tests on Si/C nanofibers.
**Electrochemical measurements:** Electrochemical testing was conducted on coin-type half cells assembled in an argon-filled glove box. A sodium foil was employed as both the reference and counter electrodes, and the self-supported Si/C film (5-10 mg) as the working electrode. 1 mol L\(^{-1}\) NaClO\(_4\) in a mixture of ethylene carbonate (EC) and polypropylene carbonate (PC) (1:1 by volume) with 5 vol% addition of fluoroethylene carbonate (FEC) was used as the electrolyte, and the Whatman glassfiber was used for the separator. Electrochemical cycling of electrodes was conducted at 50–5000 mA g\(^{-1}\) for galvanostatic measurements during the 0.01 to 3.0 V (vs. Na/Na\(^+\)) potential window. Cyclic voltammetry was performed using a Biologic VMP-3 electrochemical workstation between 0.01 and 3.0 V at scan rates of 0.1–20 mV s\(^{-1}\). The capacities were calculated based on the total mass of Si and CNFs.

**Supporting Information**

Supporting Information is available online from the Wiley Online Library or from the author.

**Acknowledgements**

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Figure 1. Electrochemical performance of the nanosized c-Si and microsized c-Si electrodes. (a) Charge-discharge profiles for selected cycles at 500 mA g$^{-1}$ (inset: the first three CV curves for the nanosized c-Si electrode), and (b) in-operando XRD data for microsized c-Si.
Figure 2. *In-operando* Raman analysis of the electrochemical behavior towards Na⁺ for the nanosized c-Si electrode. (a) *in-operando* Raman results along with the first cycle CV profile of nanosized c-Si, (b) intensity changes verses time, and (c) *ex-operando* Raman spectra of nanosized c-Si before and after the first cycle (inset: enlargement of the indicated region between 450 and 550 cm⁻¹)
Figure 3. Schematic representation of the Si/C composite design.
Figure 4. Morphology and bamboo-rattle-like architecture of the Si/C composites. SEM (above) and TEM (below) images of the synthesized Si/C samples with different SiO$_2$@Si contents: Si/C-1 (a and e), Si/C-2 (b and f), Si/C-3 (c and g), and Si/C-4 (d and h).
Figure 5. Electrochemical performance of the samples. (a) initial and second cycle charge-discharge profiles of Si/C-3 at a current density of 50 mA g$^{-1}$, (b) the first three CV curves of Si/C-3 at a scan rate of 0.1 mV s$^{-1}$, (c) the cycling performance of Si/C-3 and pure CNFs at a current rate of 50 mA g$^{-1}$, (d) the rate performance of Si/C-3 at different current densities, and (e) long-term cycling test at a current density of 5000 mA g$^{-1}$ (over 10 C).
The electrochemical mechanism of nanocrystalline silicon anode in sodium ion batteries was first studied via *in-operando* Raman and *in-operando* XRD. An irreversible structural conversion from crystalline silicon to amorphous silicon takes place during the initial cycles, leading to ultra-fast reversible sodium insertion in the newly generated amorphous silicon. Furthermore, an optimized silicon/carbon composite has been developed to further improve its electrochemical performance.

**Keyword:** nanocrystal silicon, *in-operando* analysis, anode, sodium ion battery

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Supporting Information

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In-operando mechanism analysis on nanocrystalline silicon anode material for reversible and ultra-fast sodium storage

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Figure S1. Morphology and structure of c-Si-based electrodes. (a) XRD patterns of nanosized c-Si with Rietveld refinement, (b) Schematics of the diamond cubic structure of nanosized c-Si, (c) SEM image of nanosized c-Si, (d) SEM image of microsized c-Si, and (e) the corresponding FFT pattern of HRTEM image of nanosized c-Si.
Figure S2. CV and Kinetics analysis of the electrochemical behavior towards Na\(^+\) for the nanosized c-Si electrode. (a) CV curves of nanosized c-Si-based SIB at various scan rates, from 0.1 to 20 mV s\(^{-1}\), (b) determination of the b-value during the discharge processes using the relationship between peak current and scan rate, (c) separation of the capacitive and diffusion currents in nanosized c-Si at a scan rate of 10 mV s\(^{-1}\), and (d) contribution ratio of the capacitive and diffusion-controlled charge versus scan rate.
Figure S3. The discharge capacity of micro sized c-Si at a current density of 50 mA g$^{-1}$. 
Figure S4. Morphology and structure characterization of the nanosized c-Si electrode under different electrochemical states. The corresponding FFT patterns of HRTEM images of nanosized c-Si before (a), after 1 cycle (b), and after 10 cycle test (c).
Figure S5. Schematic illustrations explaining the changes in the nanosized c-Si. (a) Original c-Si, (b) Si-Si bonds are broken and replaced by two Na-Si bonds in stable positions, (c) the propagation of Si-Si bond breaking and sliding of Na atoms induce collapse of the planes, leading to an amorphous Na-Si alloy, and (d) a-Si is obtained even after the desodiation process. (Red and yellow balls represent the interfacial Na and Si atoms, respectively)
Figure S6. The comparison among microsized c-Si, nanosized c-Si and the structure optimized Si/C composite during the first sodiation/desodiation process.
Figure S7. SEM image of SiO$_2$@Si
Figure S8. The model of the three-point bend test under AFM system (a), relationship between distance and voltage for the different samples (b), pictures of different states of a piece of Si/C-3 nanofiber web under fold and release process (c), the XRD profiles of the Si/C-3 sample and the pure CNFs (d), and TG curve of Si/C-3 under air (e).
### Table S1. Comparison of the presented alloy anode materials in SIBs.

<table>
<thead>
<tr>
<th>Category</th>
<th>Name</th>
<th>Reversible capacity (mAh g(^{-1}))</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sb-based anodes</td>
<td>Pure Sb</td>
<td>660(^{[1]})</td>
</tr>
<tr>
<td></td>
<td>Amorphous AlSb</td>
<td>~ 400(^{[2]})</td>
</tr>
<tr>
<td></td>
<td>Amorphous MoS(_2)Sb</td>
<td>~ 400(^{[3]})</td>
</tr>
<tr>
<td></td>
<td>Sb/C</td>
<td>640(^{[4]})</td>
</tr>
<tr>
<td>Sn-based anodes</td>
<td>Pure Sn</td>
<td>847(^{[5]})</td>
</tr>
<tr>
<td></td>
<td>Sn/Ni</td>
<td>730(^{[6]})</td>
</tr>
<tr>
<td></td>
<td>Sn(<em>{0.9})Cu(</em>{0.1})</td>
<td>420(^{[4]})</td>
</tr>
<tr>
<td></td>
<td>SnSb</td>
<td>500(^{[8]})</td>
</tr>
<tr>
<td>Ge-based anodes</td>
<td>Pure amorphous Ge</td>
<td>400(^{[9]})</td>
</tr>
<tr>
<td></td>
<td>Ge thin films</td>
<td>&lt; 369(^{[10]})</td>
</tr>
<tr>
<td>In-based anodes</td>
<td>Pure In</td>
<td>467(^{[11]})</td>
</tr>
<tr>
<td></td>
<td>In thin film</td>
<td>&lt; 100(^{[11]})</td>
</tr>
<tr>
<td>Si-based anodes</td>
<td>Pure c-Si</td>
<td>960(^{[12-13]})</td>
</tr>
<tr>
<td></td>
<td>Si/C</td>
<td>450</td>
</tr>
</tbody>
</table>


Figure S9. The TEM images of Si/C-3 after cycling test.